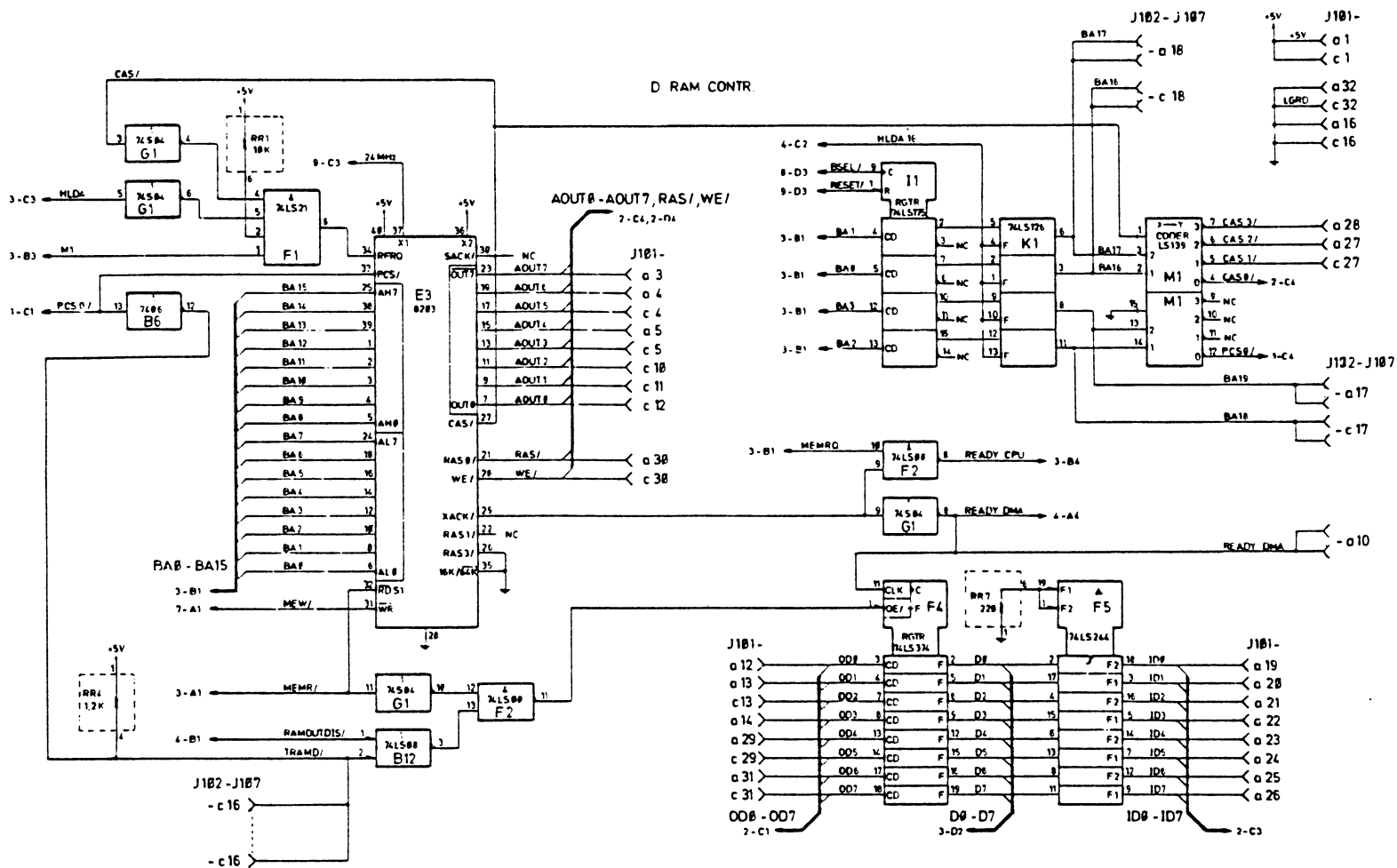
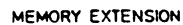


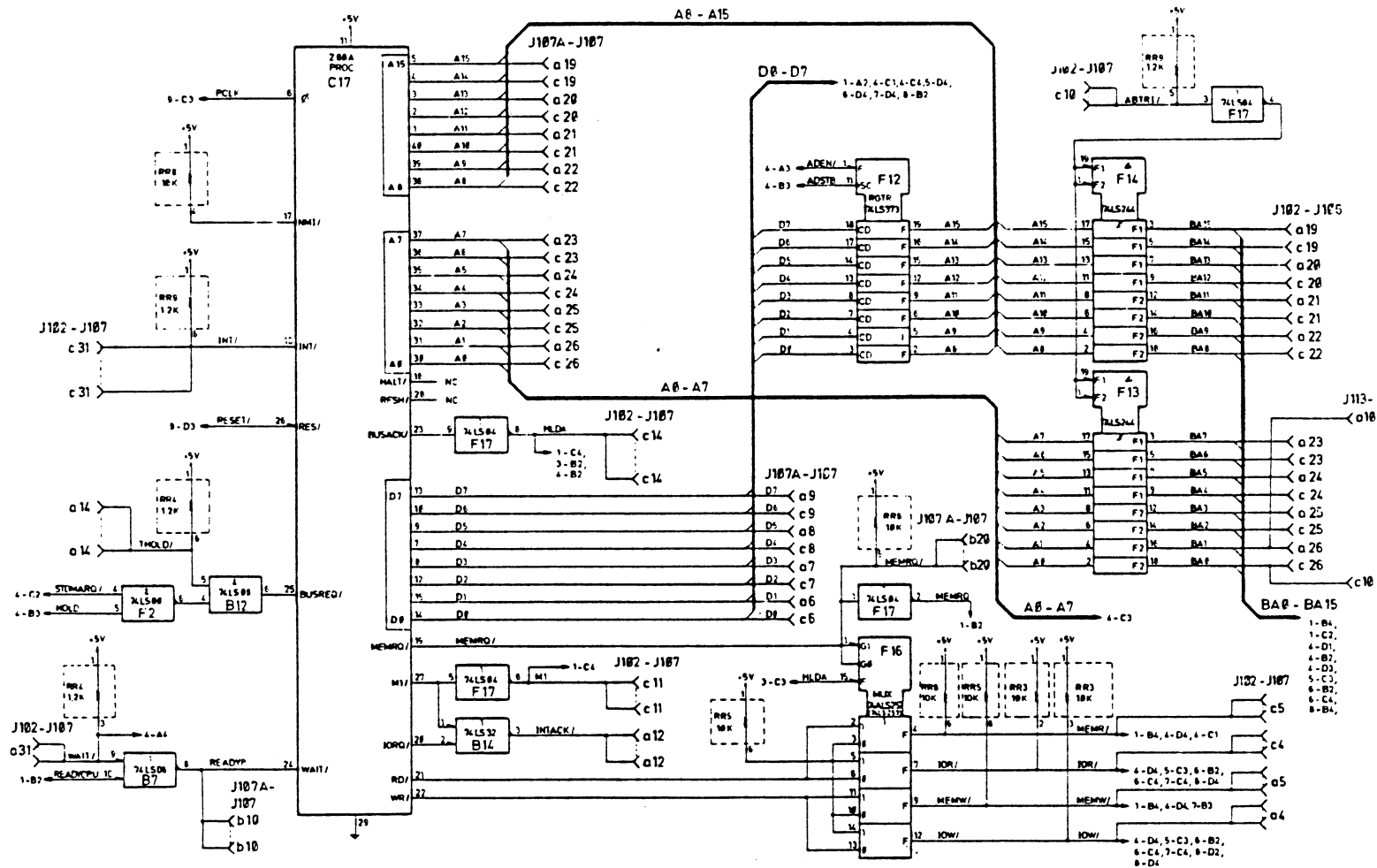
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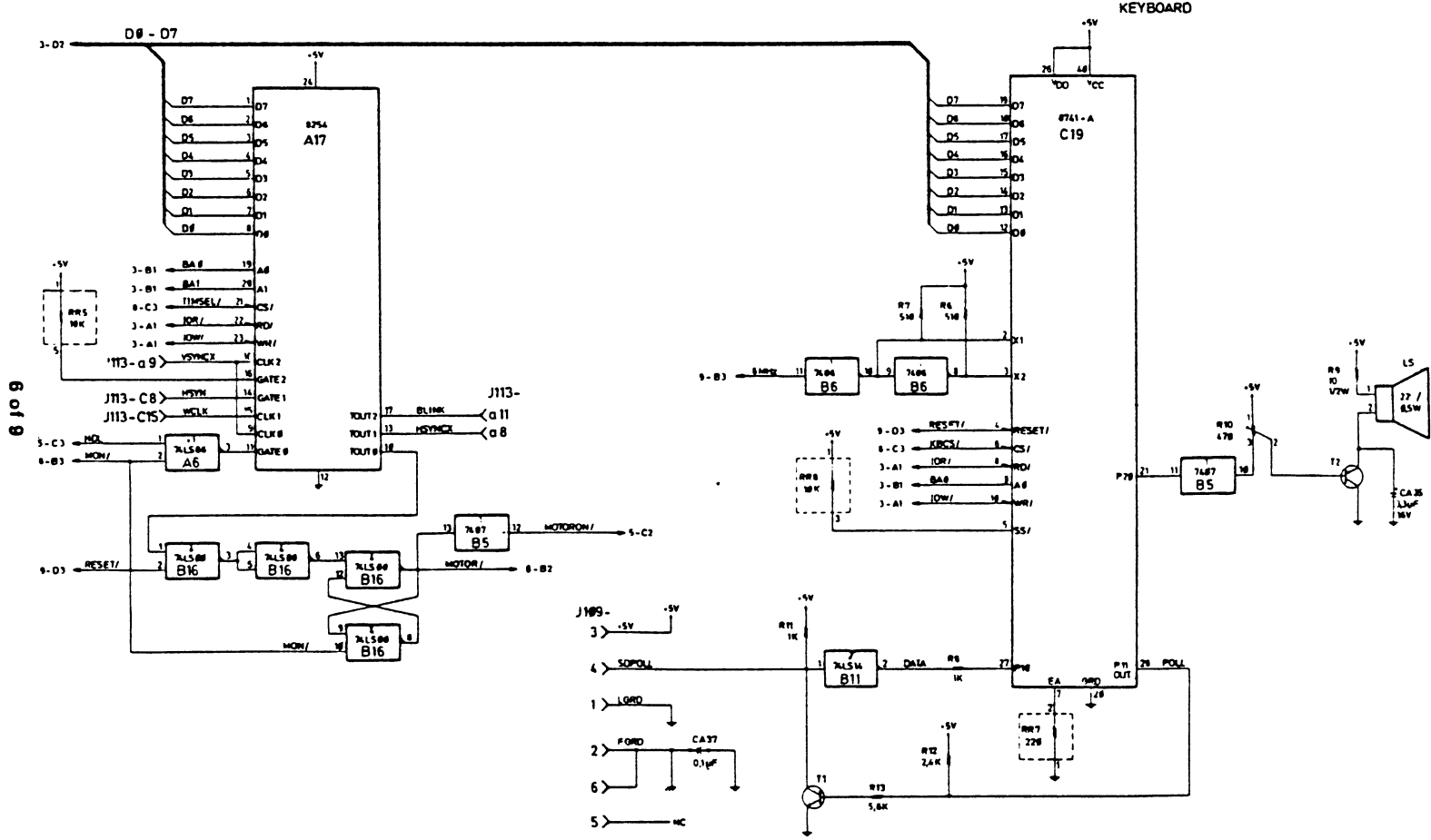


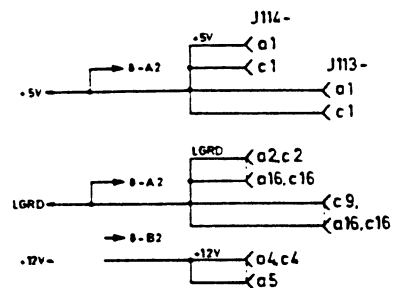
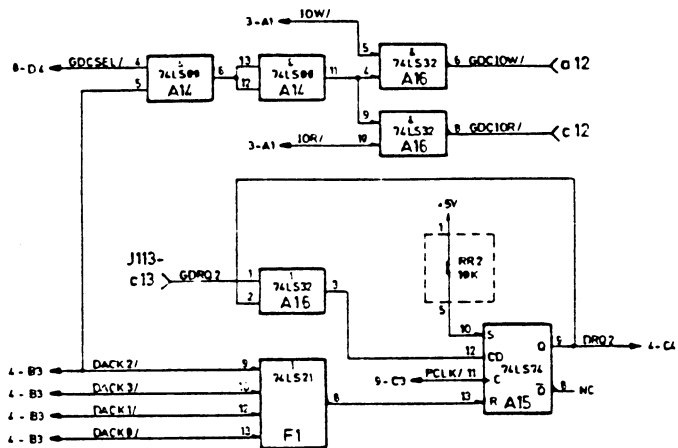
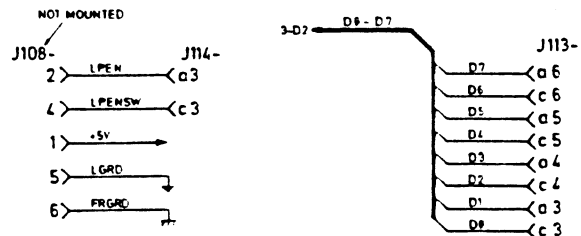
8		4	
1	4V	1	4V
2	MC	2	MC
3	ABOUT 7	3	MC
4	ABOUT 6	4	ABOUT 5
5	ABOUT 4	5	ABOUT 3
6	MC	6	MC
7	MC	7	MC
8	MC	8	MC
9	MC	9	MC
10	MC	10	ABOUT 7
11	MC	11	ABOUT 6
12	OOB	12	ABOUT 8
13	OO1	13	OO2
14	OO3	14	MC
15	MC	15	MC
16	LGRO	16	LGRO
17	MC	17	MC
18	MC	18	MC
19	IO B	19	MC
20	IO 1	20	MC
21	IO 2	21	MC
22	IO 3	22	MC
23	IO 4	23	MC
24	IO 5	24	MC
25	IO 6	25	MC
26	IO 7	26	MC
27	CAS 2 /	27	CAS 1 /
28	CAS 3 /	28	MC
29	OOA	29	OO5
30	PLS-7	30	MC
31	PLS-7	31	MC
32	LGRO	32	LGRO











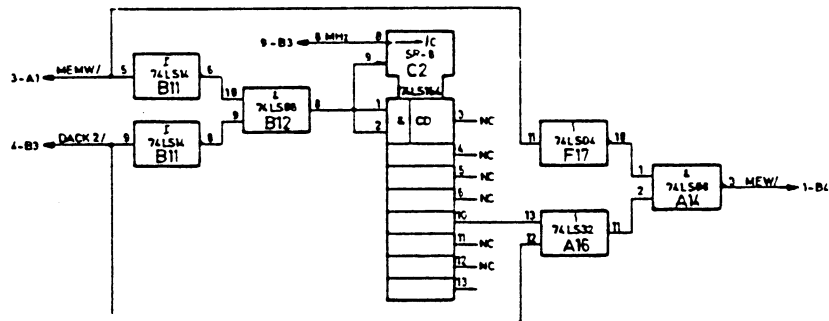
TO GRAPHIC BOARD

J114

E		E
+5V	1	+5V
LGRO	2	LGRO
LPEN	3	LPENSW
D5	4	+12V
+12V	5	MC
MC	6	MC
MC	7	MC
MC	8	MC
MC	9	MC
MC	10	MC
MC	11	MC
MC	12	MC
MC	13	MC
MC	14	MC
MC	15	LGRO
LGRO	16	LGRO

J113

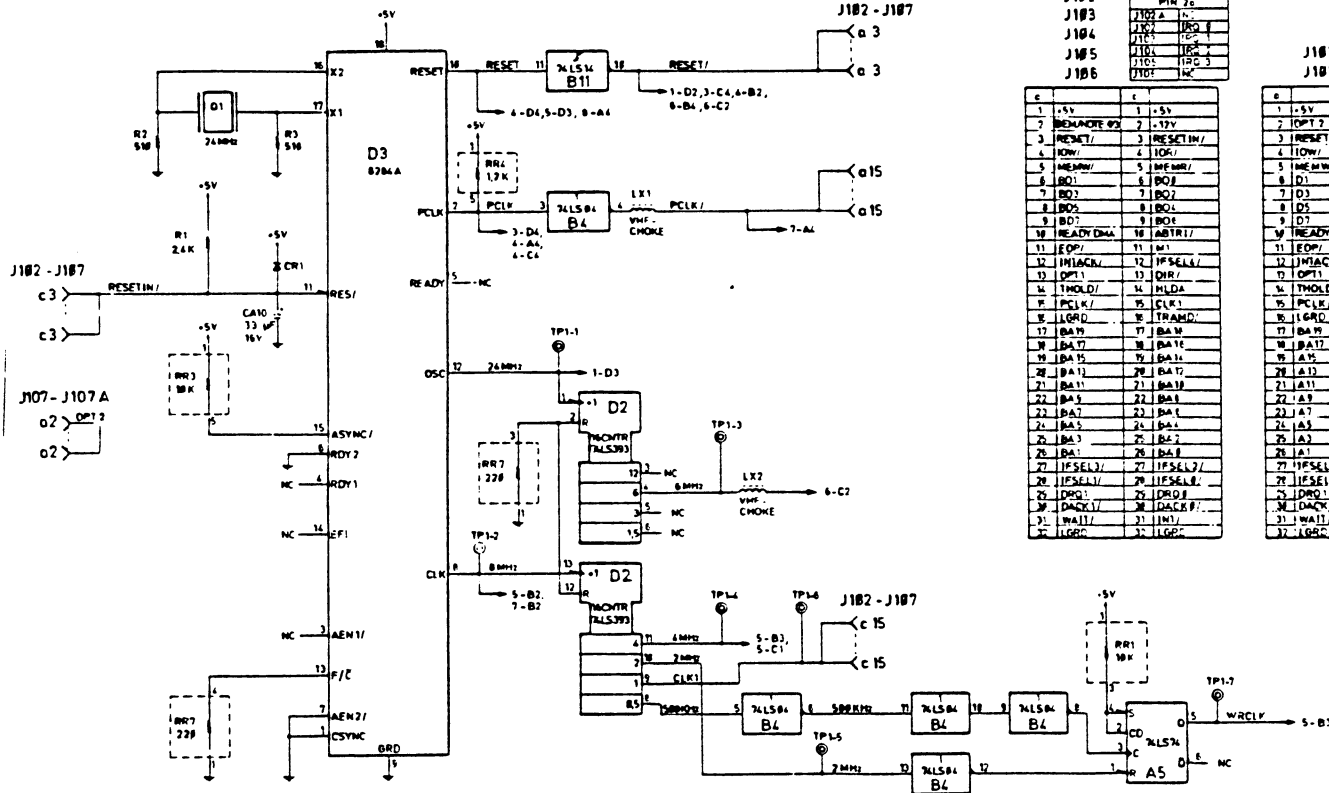
E		E
+5V	1	+5V
MC	2	MC
D1	3	D8
D3	4	D7
D5	5	D4
D7	6	D6
MC	7	MC
HSYNCK	8	HSYN
VSYNCK	9	LGRO
BA1	10	BA8
BLINK	11	MC
GDC10W/	12	GDC10R/
DACK2/	13	GDRQ2
MC	14	MC
MC	15	MC
LGRO	16	LGRO



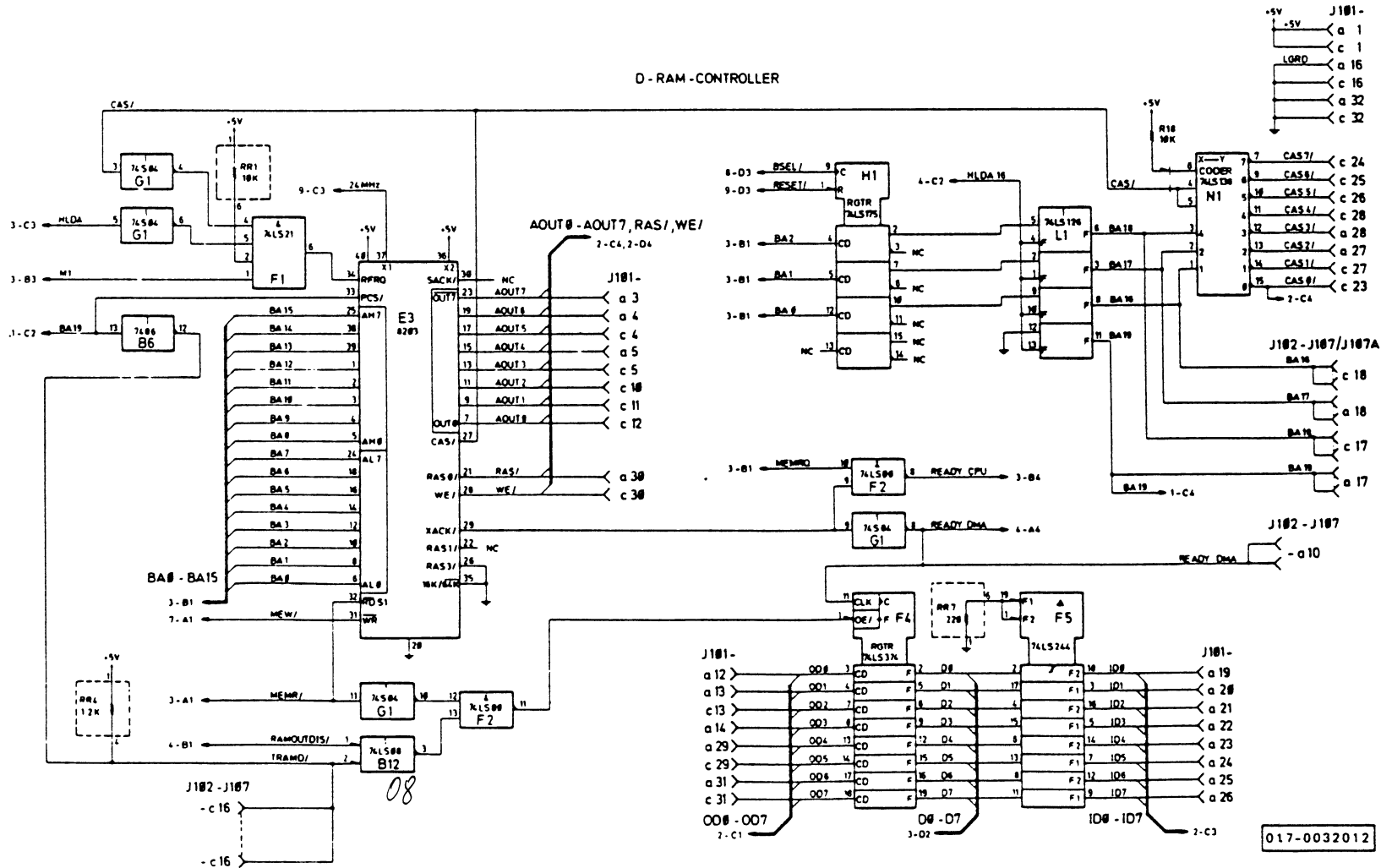


e	t
1 .55	1 .55
2 BENJAMIN 02	2 .12V
3 BENJAMIN	3 RESISTIN
4 BOW	4 LOG
5 BOW	5 BOW
6 BOZ	6 BOZ
7 BOZ	7 BOZ
8 BOZ	8 BOZ
9 BOZ	9 BOZ
10 READY ONE	10 ABSTIN
11 PACE	11 PACE
12 PHILIP	12 PHELIX
13 PHILIP	13 PHILIP
14 PHILIP	14 PHILIP
15 PHILIP	15 PHILIP
16 PHILIP	16 PHILIP
17 PHILIP	17 PHILIP
18 PHILIP	18 PHILIP
19 PHILIP	19 PHILIP
20 PHILIP	20 PHILIP
21 PHILIP	21 PHILIP
22 PHILIP	22 PHILIP
23 PHILIP	23 PHILIP
24 PHILIP	24 PHILIP
25 PHILIP	25 PHILIP
26 PHILIP	26 PHILIP
27 PHILIP	27 PHILIP
28 PHILIP	28 PHILIP
29 PHILIP	29 PHILIP
30 PHILIP	30 PHILIP
31 PHILIP	31 PHILIP
32 PHILIP	32 PHILIP

a	b	c
1 -5V	1 -5V	1 -5V
2 IOP2.7	2	2 -5V
3 RESET/	3	3 RESET/IN
4 IOW/	4 IOW/	4
5 M5W/	5 M5W/	5 MEMR/
6 D1	6	6 D1
7 D3	7	7 D2
8 D5	8	8 D4
9 D7	9	9 D7
W READY DMA	W READY	W DTR/
10	10 HOLD	10 M1
12 UNIMACK	12 SWITCHIN	12 IFSEL4 /
13 D10	13 MDAE	13
14 THOLD	14 MBIW/	14 MDA
15 PCLK/	15 SDRAMQ/	15 CLK1
16 LGD	16 LGD	16 TRAMC/
17 BAH	17 MBISET	17 BAH
18 IAD	18	18
19 A5	19	19 A4
20 A13	20 MDSIO	20 A2
21 A11	21	21 A4
22 A9	22	22 A8
23 A7	23	23 A6
24 A5	24	24 A4
25 A3	25	25 A2
26 A1	26	26 A0
27 IFSSEL3	27	27 IFSSEL2
28 IFSSEL1	28	28 IFSSEL0
29 DRD	29	29 DRD
30 DACK	30	30 DACK
31 WAIT	31	31 INT1
32 LGD	32 LGD	32 LGD

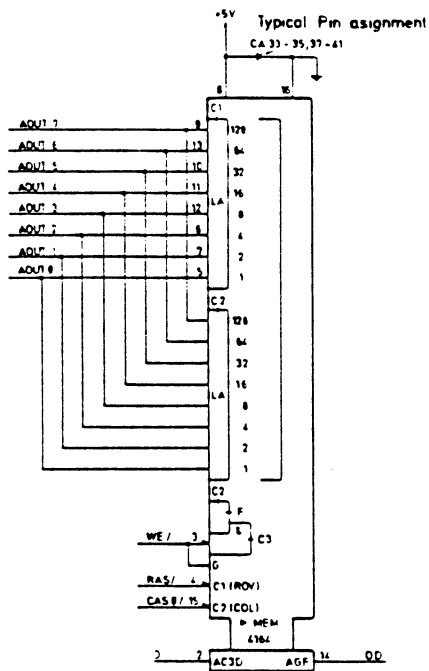


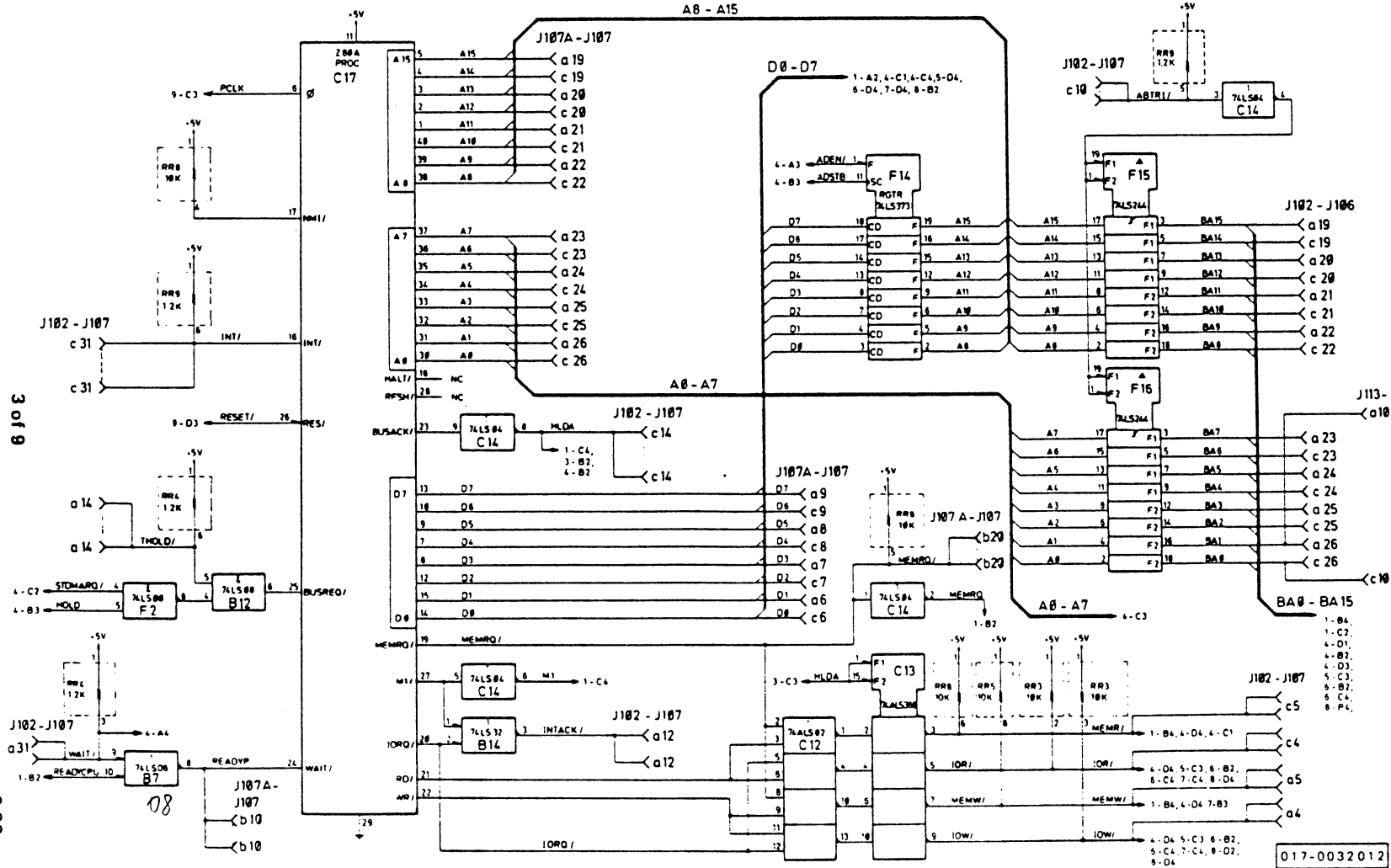
D - RAM - CONTROLLER

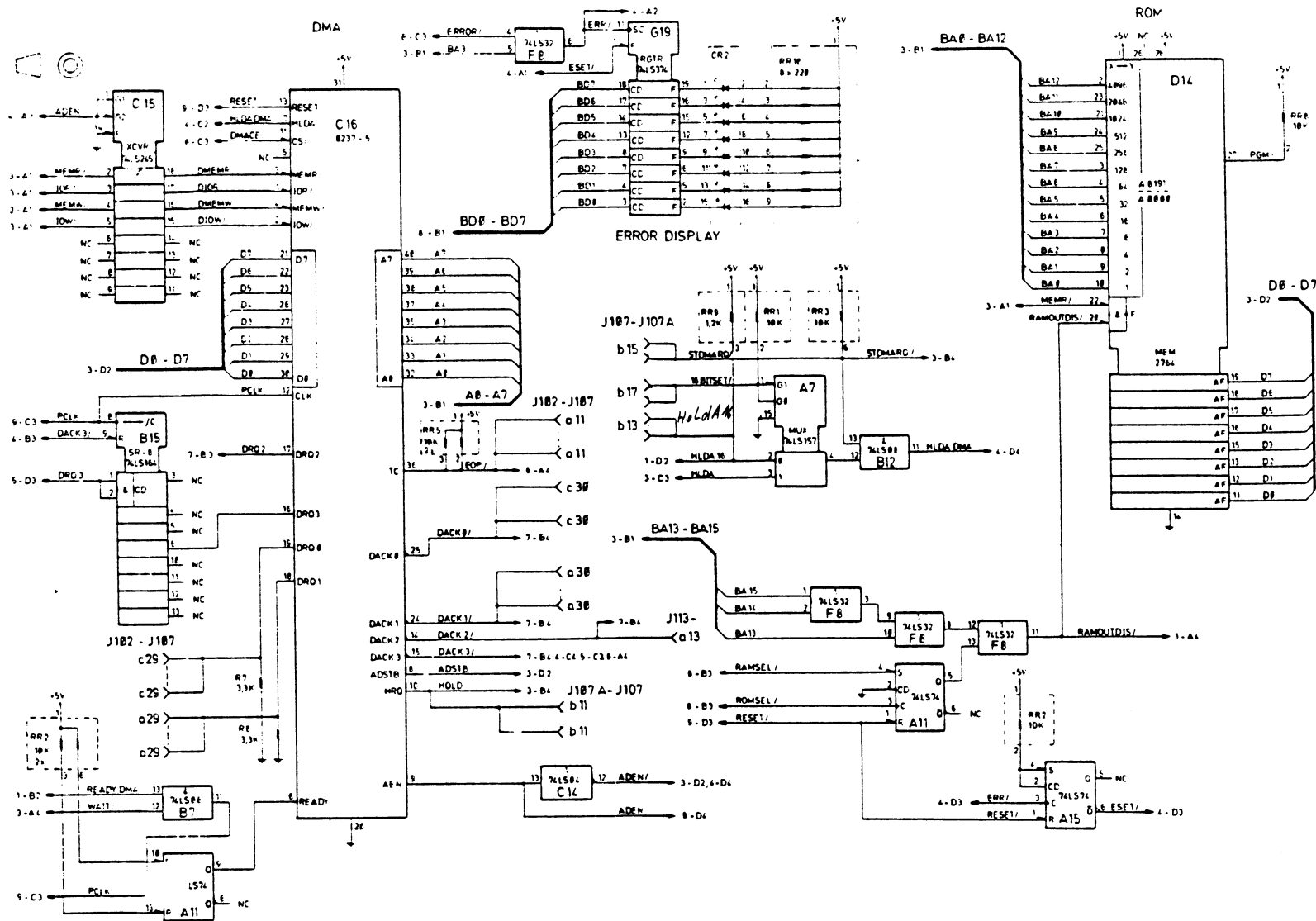




0		4	
1	1AY	5	1BY
2	1MC	6	1MC
3	1AOUT 7	7	1MC
4	1AOUT 8	8	1AOUT 5
5	1AOUT 1	9	1AOUT 3
6	1MC	10	1MC
7	1MC	11	1MC
8	1MC	12	1MC
9	1MC	13	1MC
10	1MC	14	1AOUT 2
11	1MC	15	1AOUT 1
12	1DOJ 1	16	1DOJ 8
13	1DO 1	17	1DO 2
14	1DO 3	18	1MC
15	1MC	19	1MC
16	1LGRO 1	20	1LGRO
17	1MC	21	1MC
18	1MC	22	1MC
19	1DO 8	23	1MC
20	1DO 10	24	1MC
21	1DO 2	25	1MC
22	1DO 3	26	1MC
23	1DO 4	27	1CAS 8
24	1DO 5	28	1CAS 7
25	1DO 6	29	1CAS 8
26	1DO 7	30	1CAS 5
27	1CAS 2	31	1CAS 1
28	1CAS 3	32	1CAS 4
29	1DO 9	33	1DO 9
30	1CAS 6	34	1DO 7
31	1DO 3	35	1DO 7
32	1LGRO 2	36	1DO 7

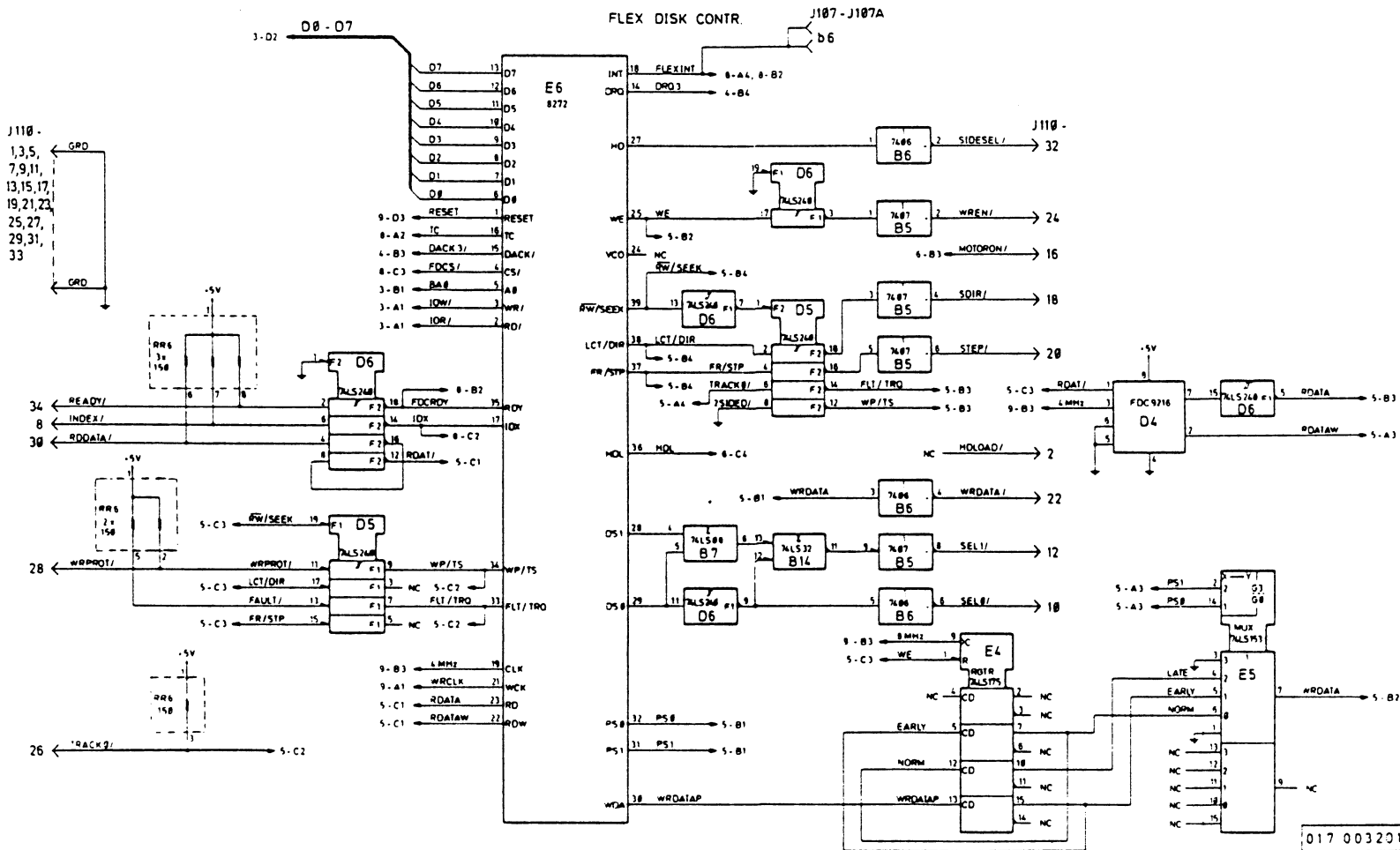




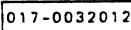


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8-25



017 0032012



TO GRAPHIC BOARD

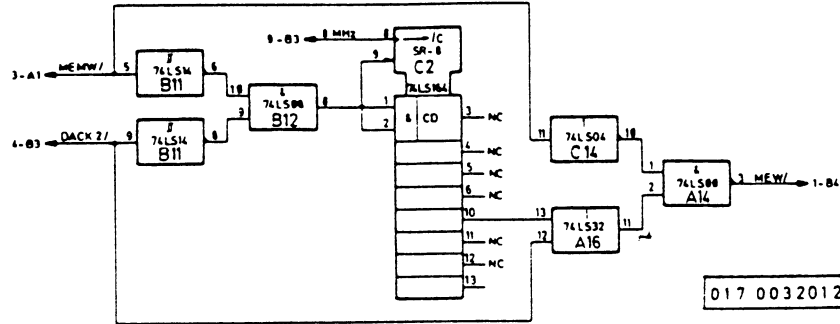
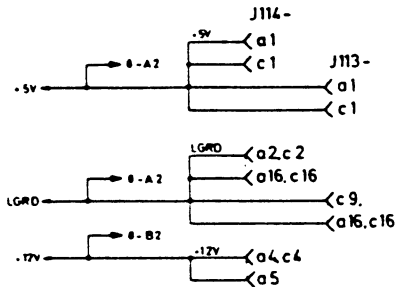
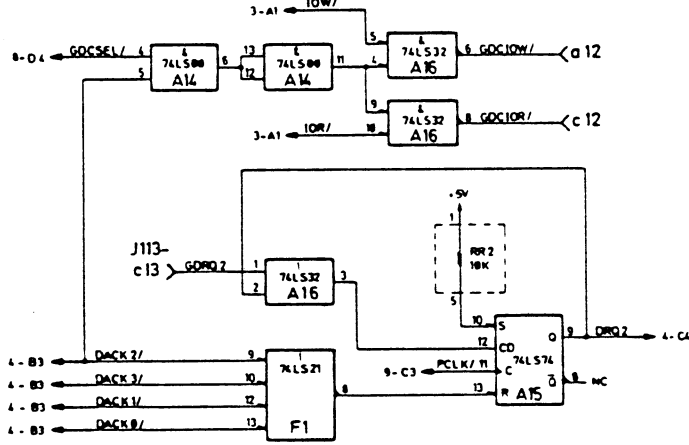
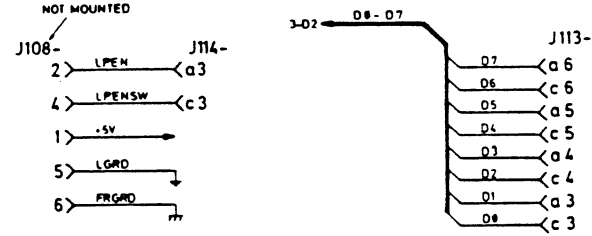
J114

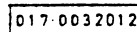
B	1	C
-5V	1	-5V
LGRO	2	LGRO
LPEN	3	LPENSW
-12V	4	-12V
-12V	5	MC
MC	6	MC
MC	7	MC
MC	8	MC
MC	9	MC
MC	10	MC
MC	11	MC
MC	12	MC
MC	13	MC
MC	14	MC
MC	15	LGRO
LGRO	16	LGRO

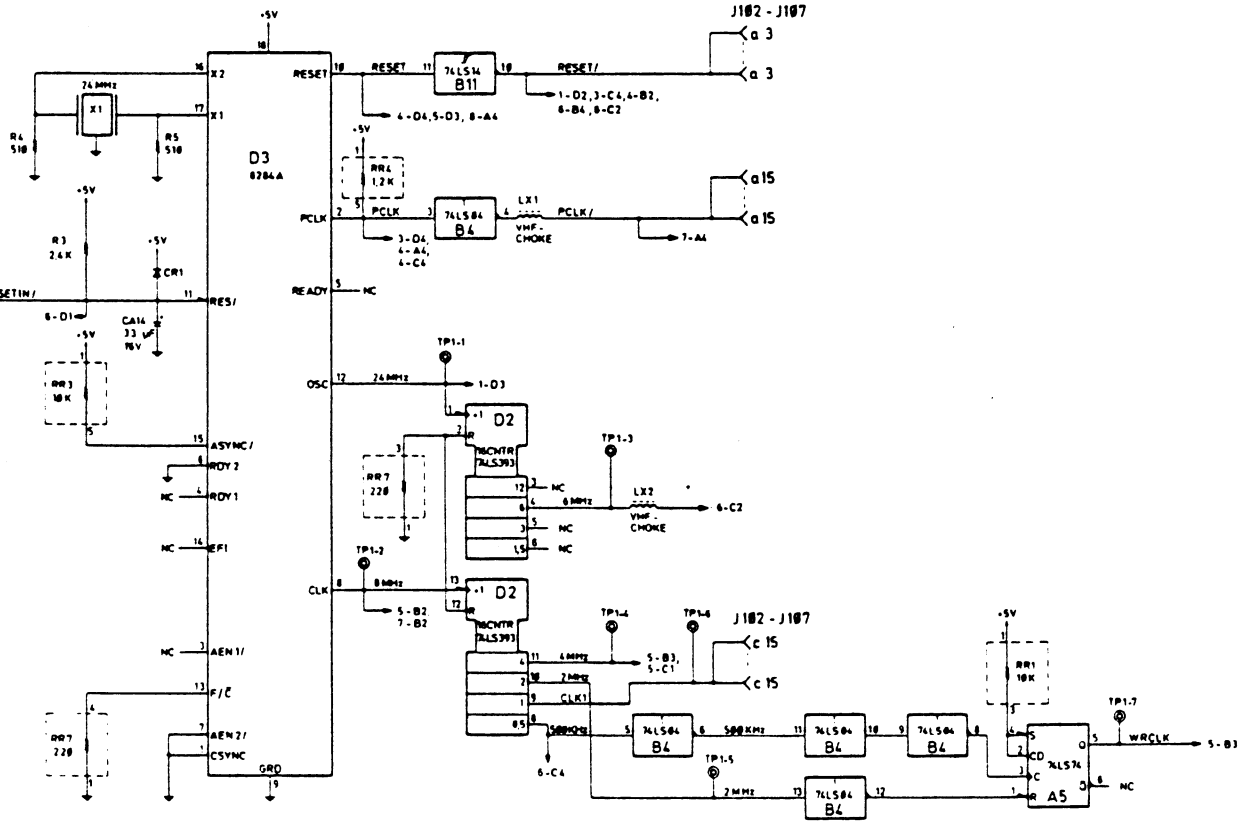
J113

B	1	C
-5V	1	-5V
MC	2	MC
D1	3	D8
D3	4	D7
D5	5	D4
D7	6	D6
MC	7	MC
H5THCX	8	H5TH
V5THCX	9	LGRO
BA1	10	BA8
BLINK	11	MC
GOC10W	12	GOC10W
DACK 2/	13	LGRO 2
MC	14	MC
MC	15	WCLK
LGRO	16	LGRO

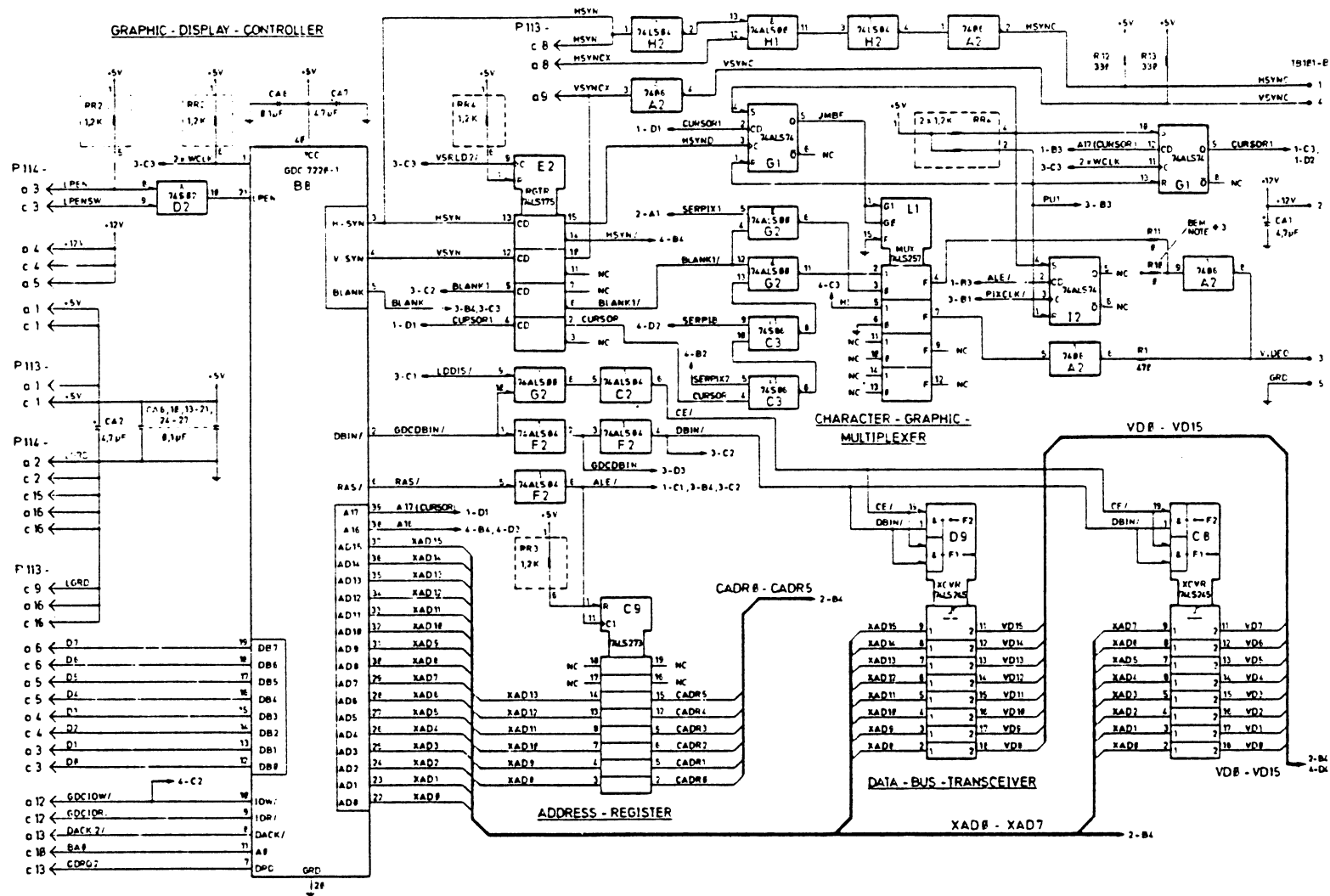
7 of 9





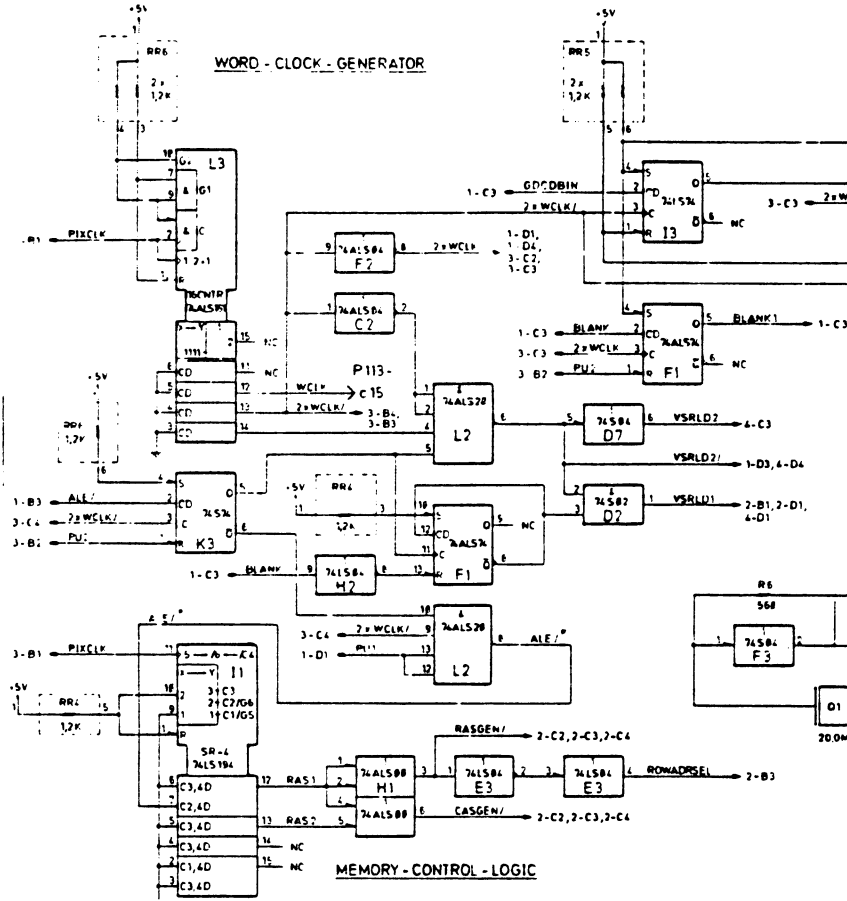


017 0032012



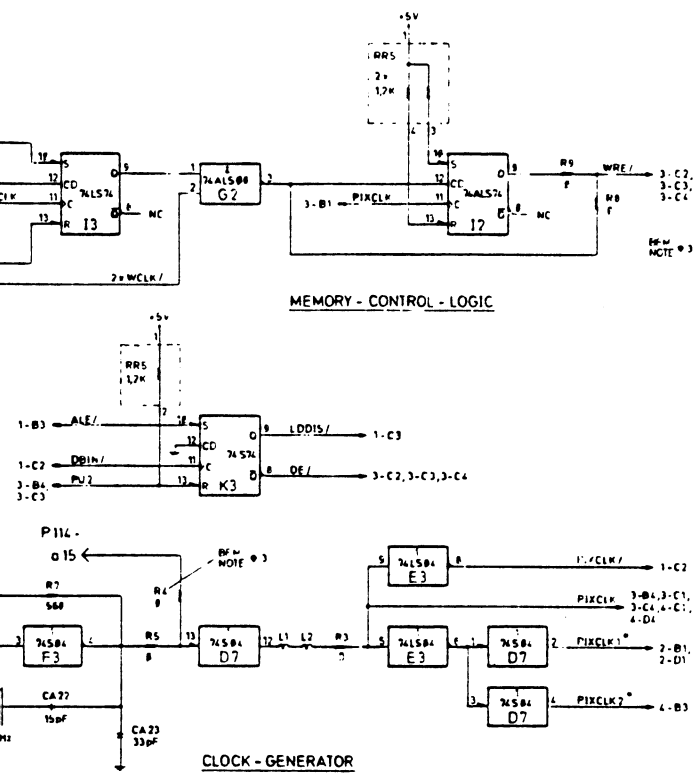


WORD - CLOCK - GENERATOR

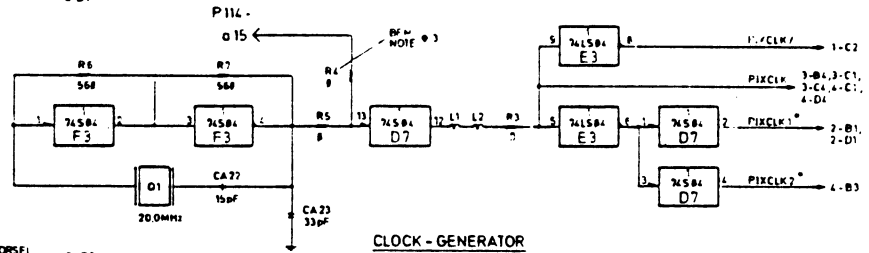


MEMORY - CONTROL - LOGIC

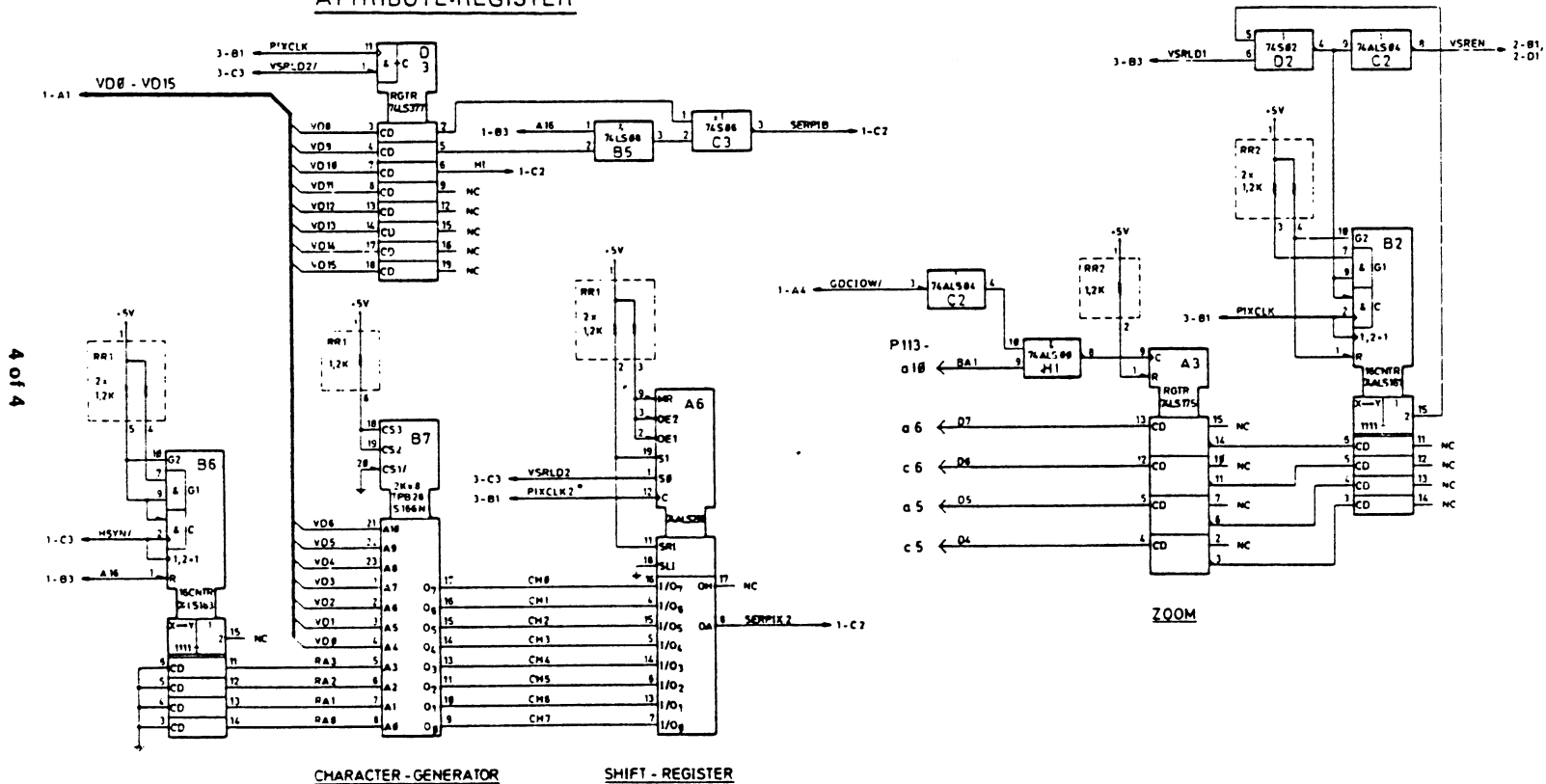
MEMORY - CONTROL - LOGIC

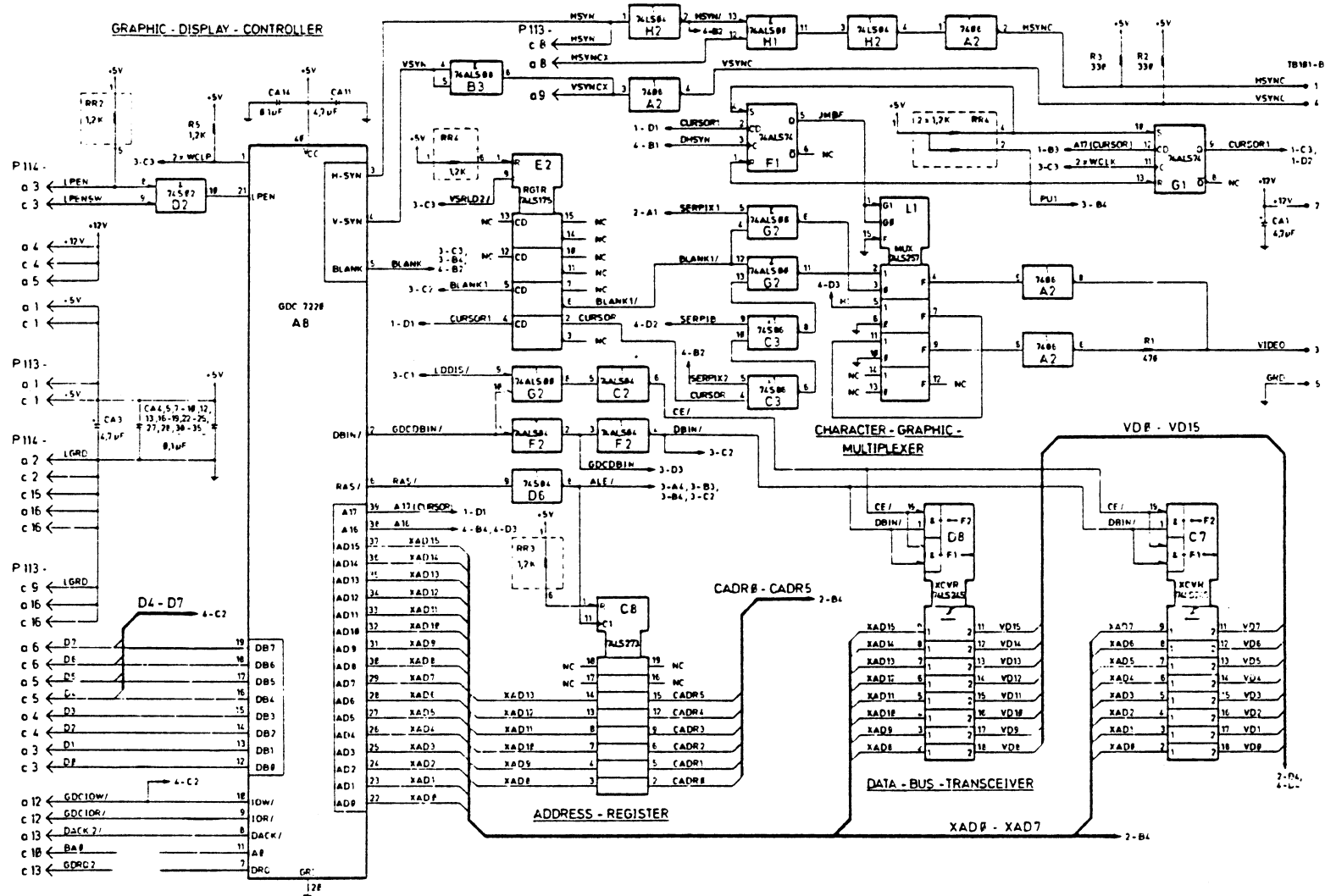


CLOCK - GENERATOR

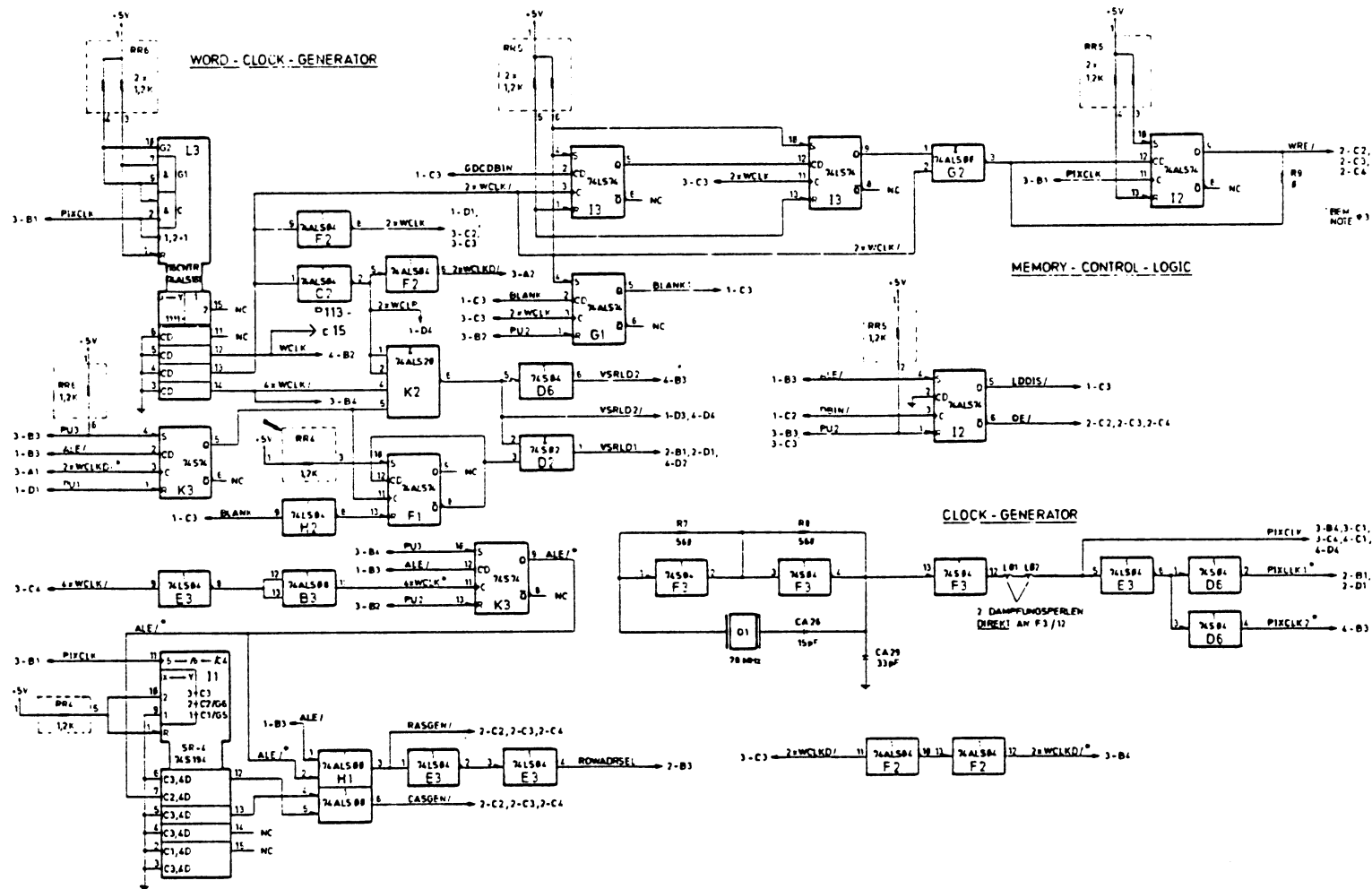


ATTRIBUTE-REGISTER

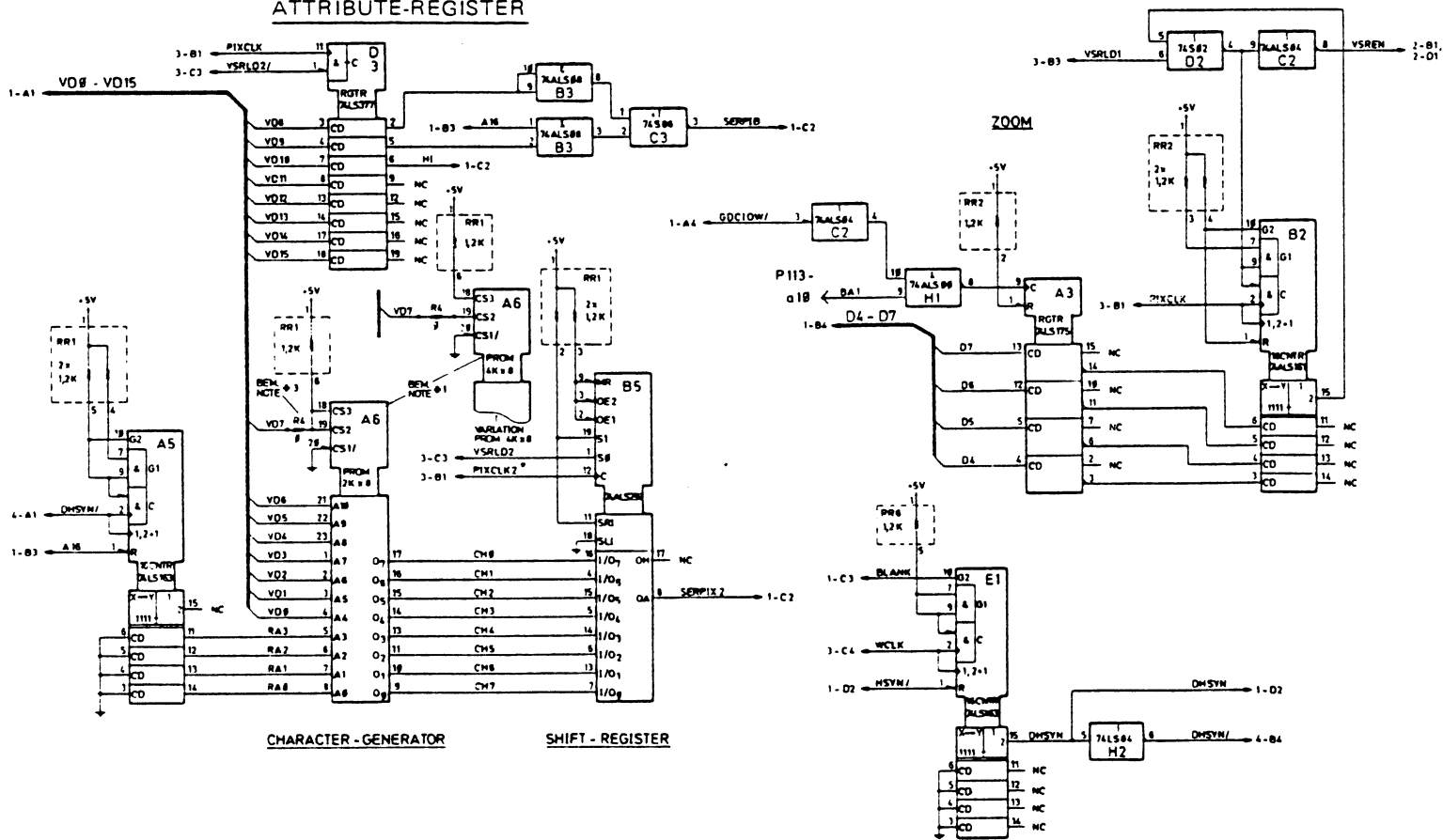




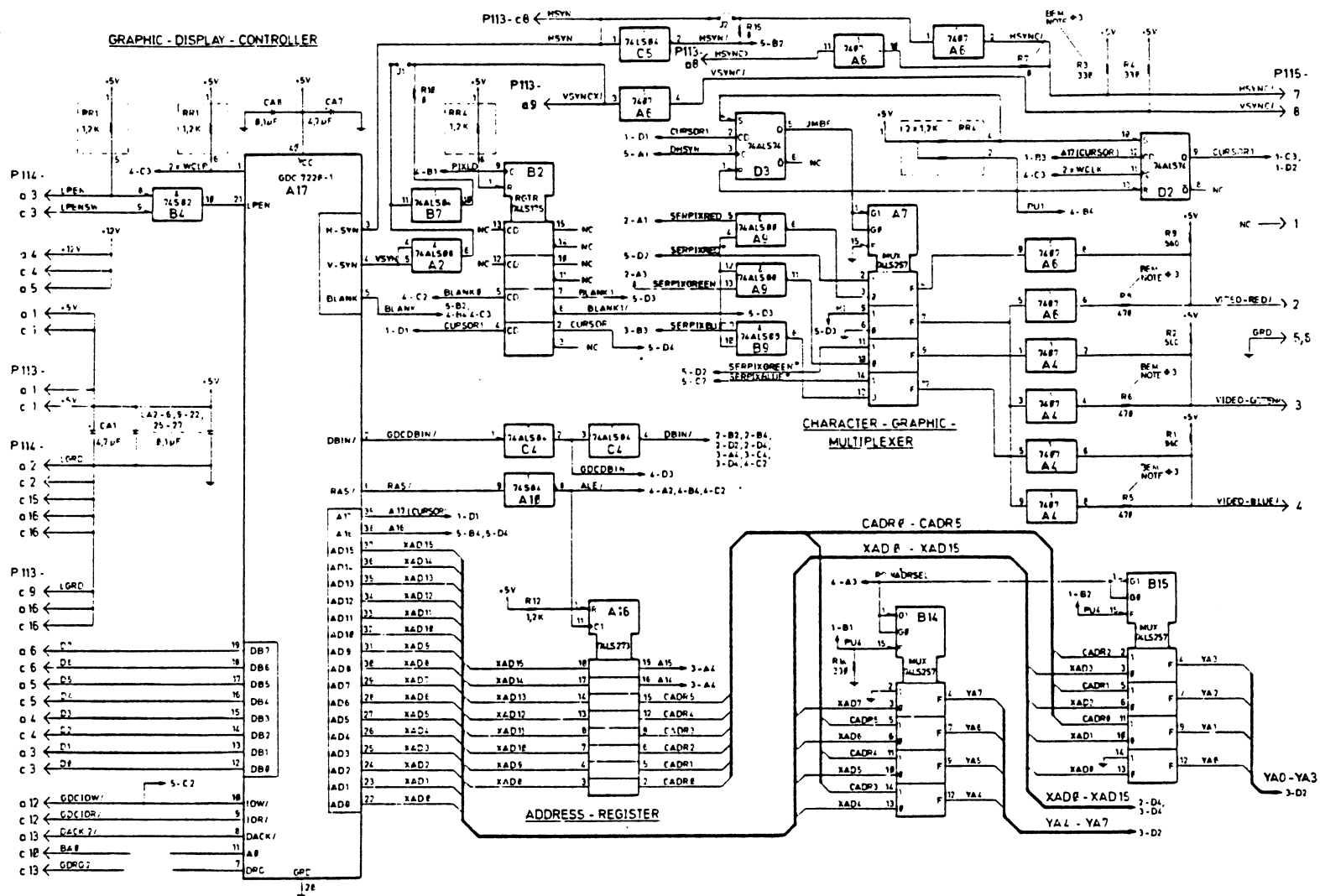


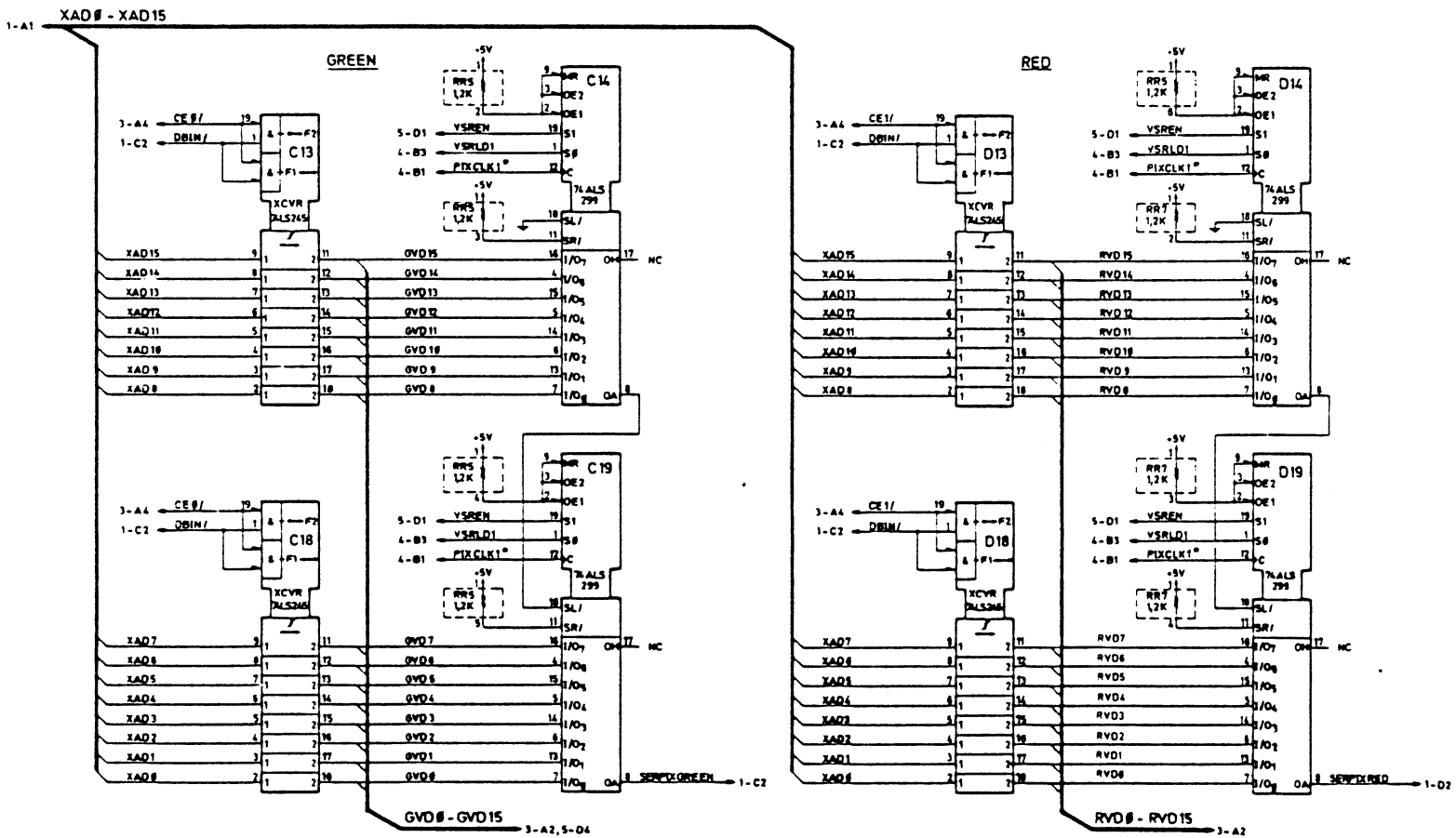


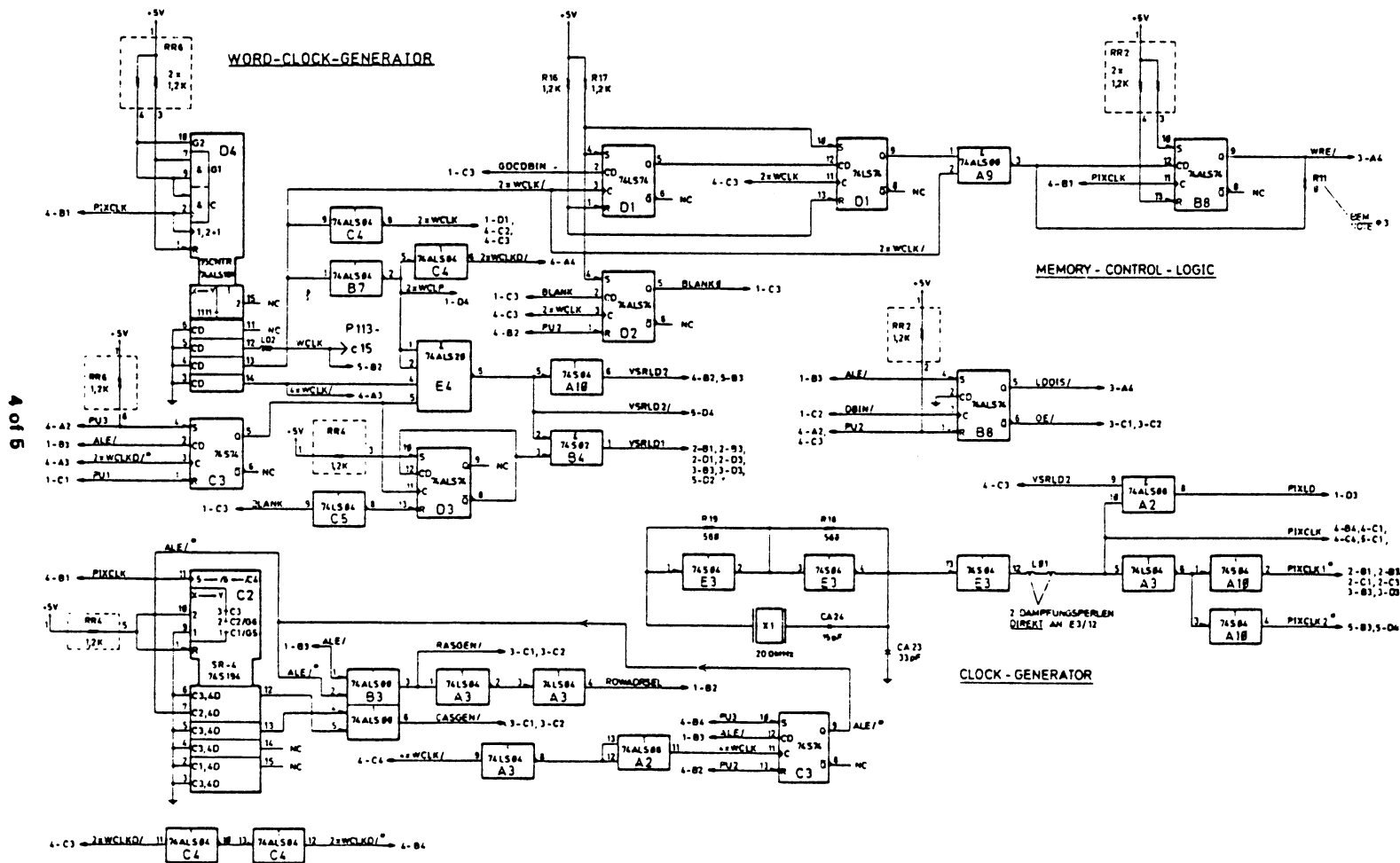
ATTRIBUTE-REGISTER



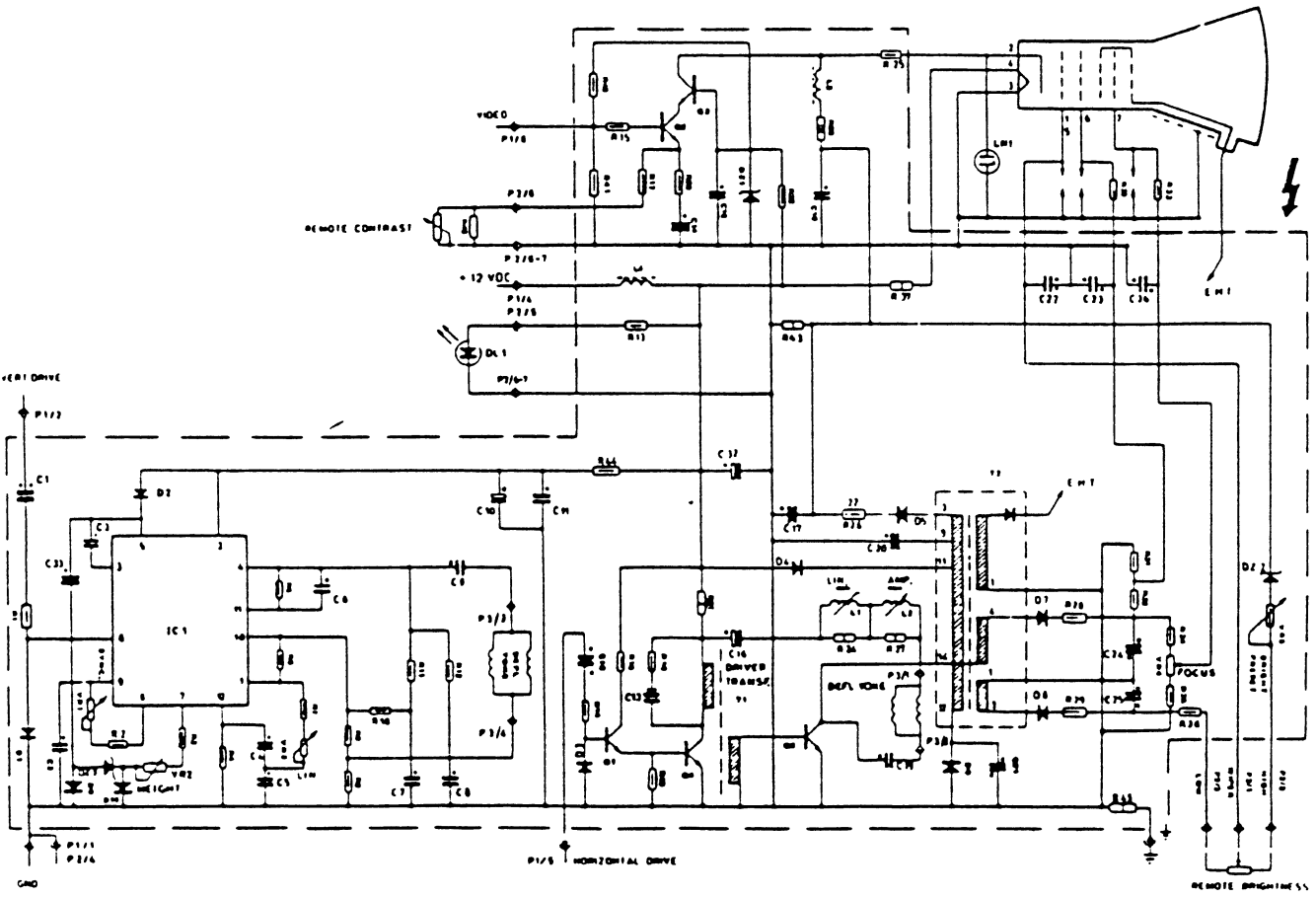
4 of 4

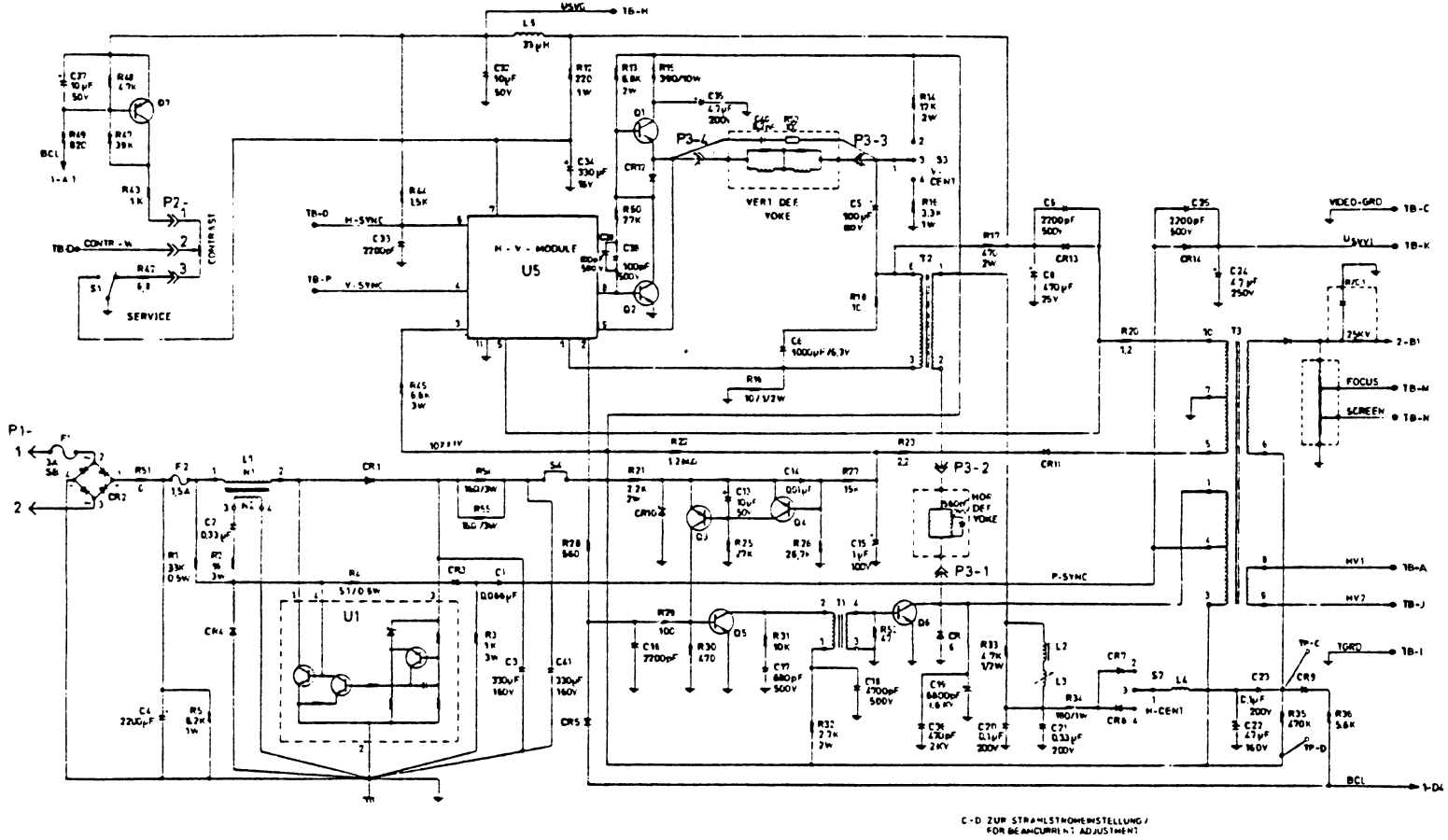




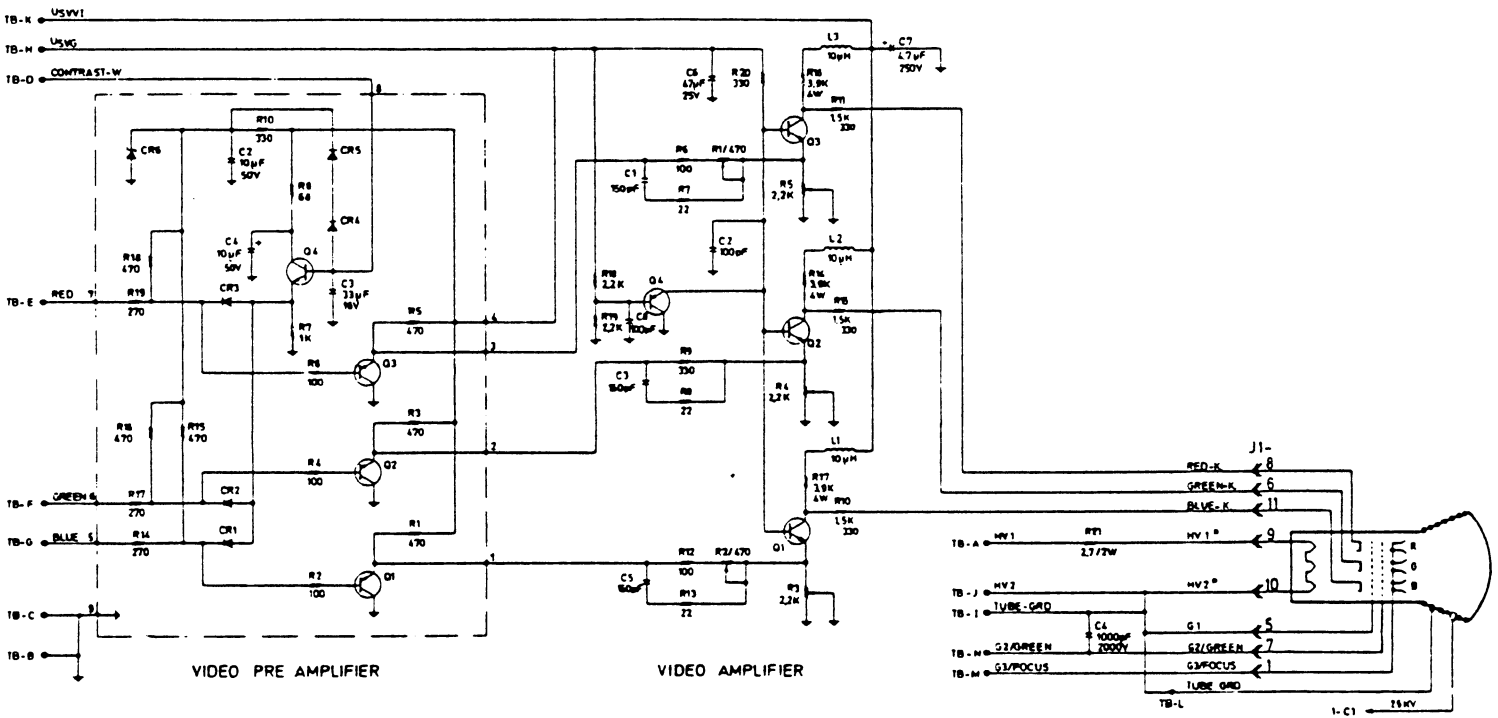








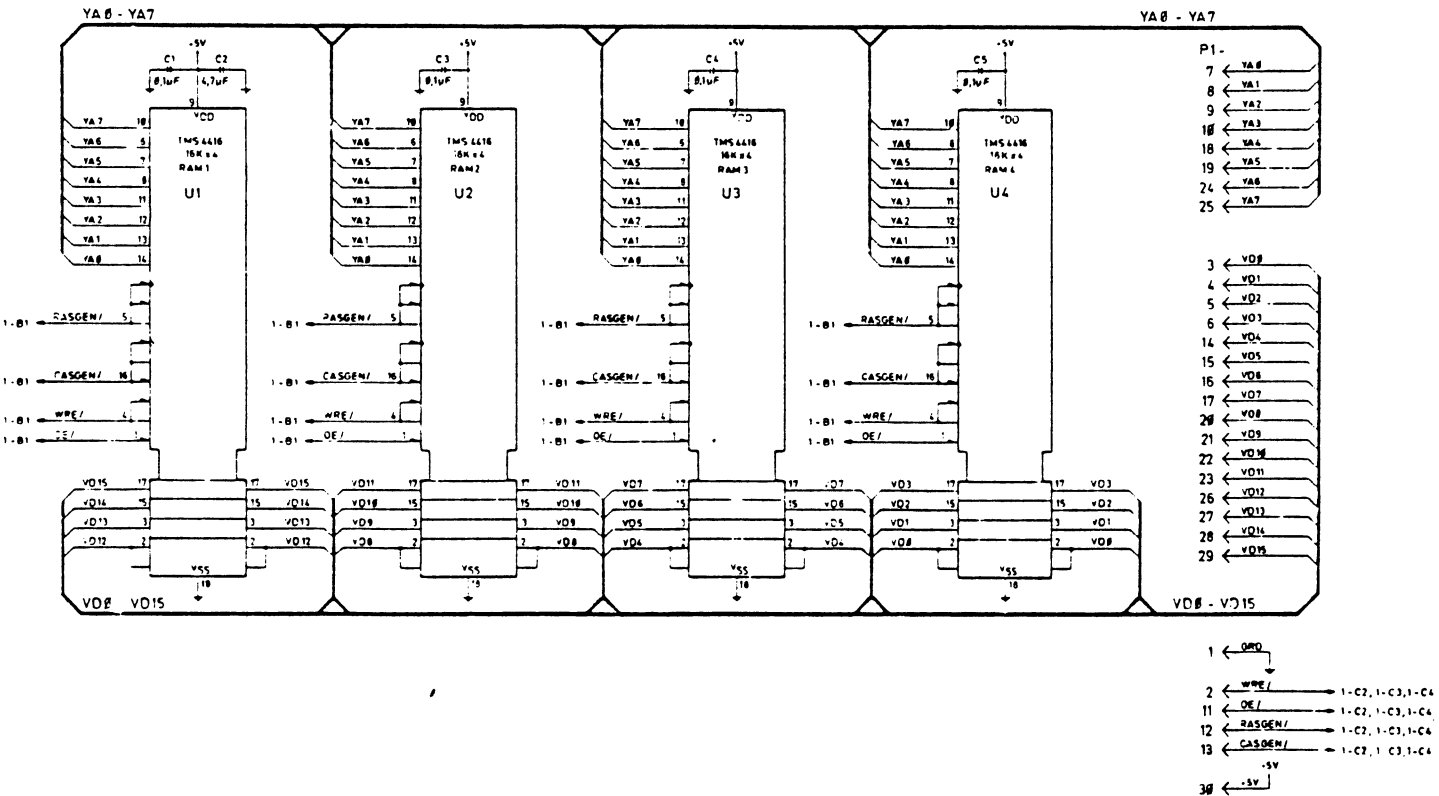
C-D ZUR STRAHLESTROMEINSTELLUNG /
FOR BEAM CURRENT ADJUSTMENT



Video Deflection Color 2 of 3

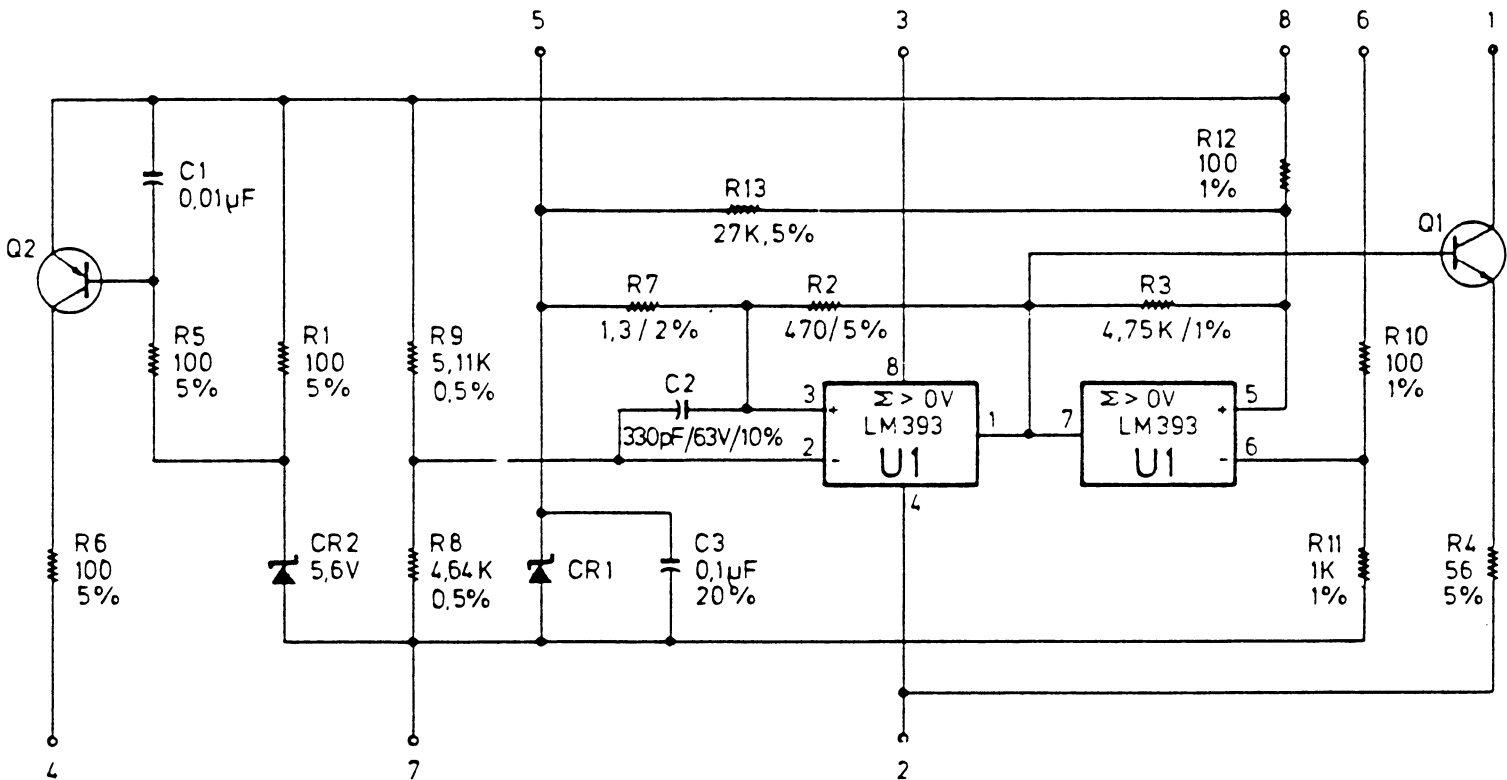
[illegible]

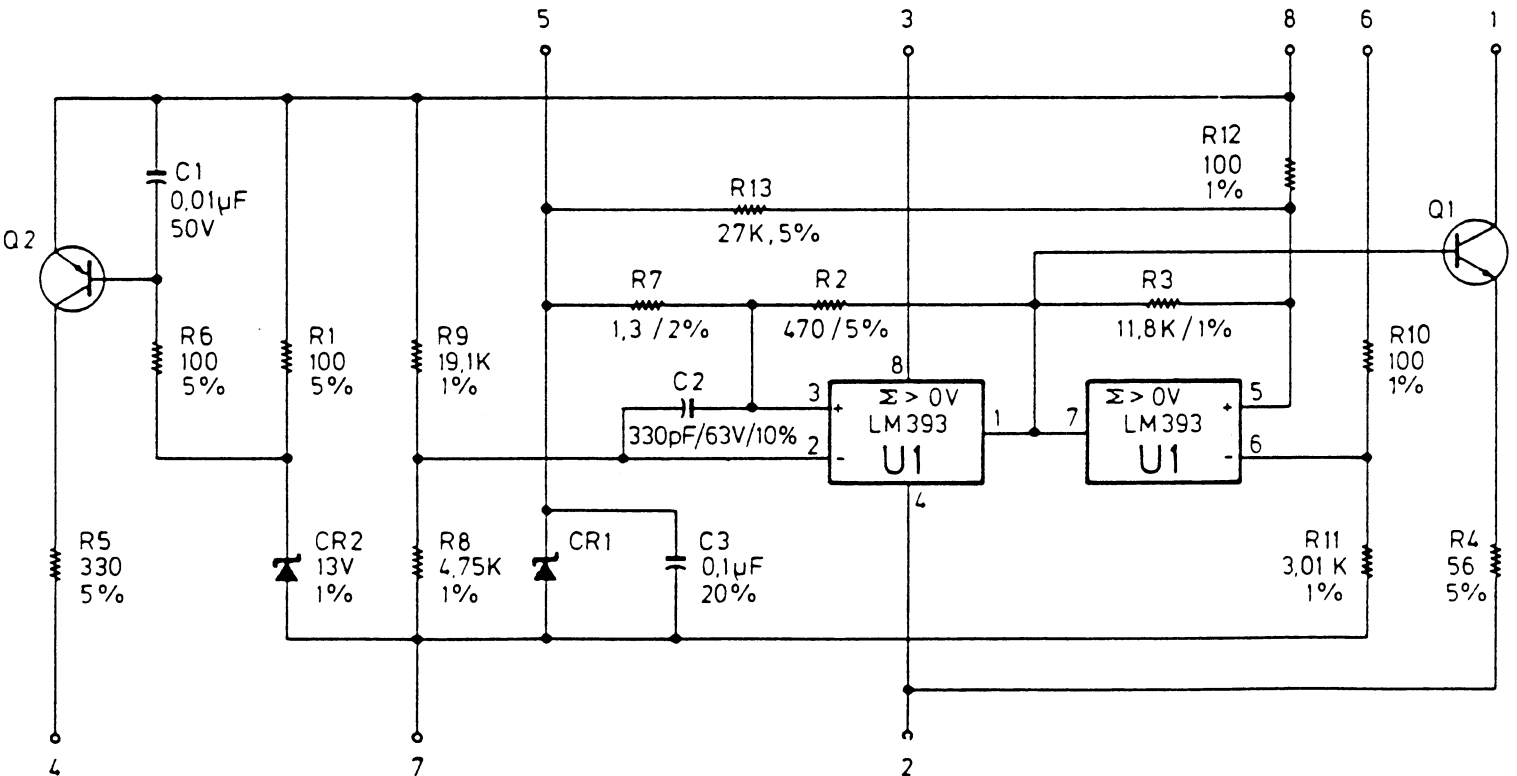
Video Deflection Color 3 of 3



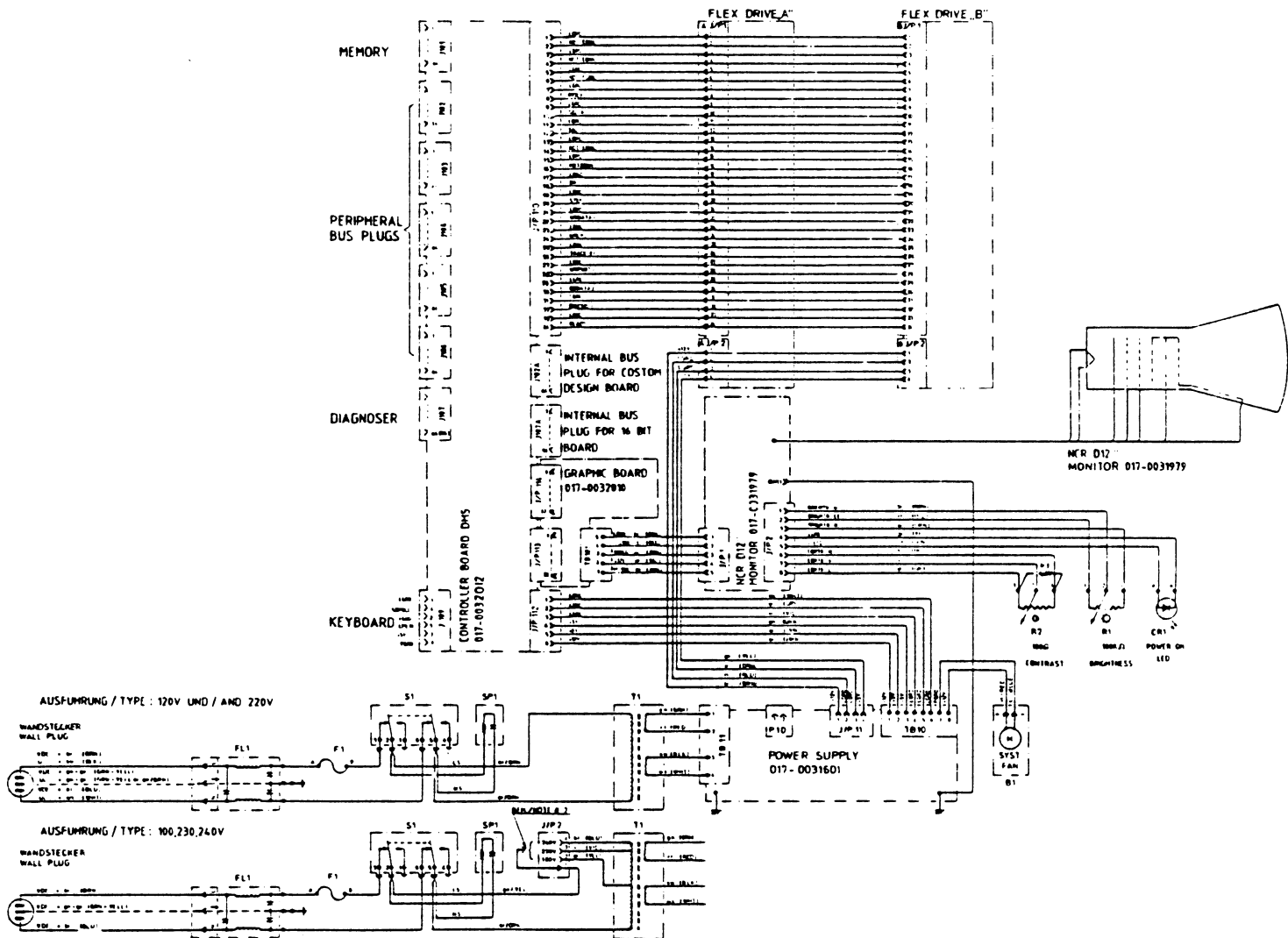
Memory Hybrid 32K 0170032511 Rev. B

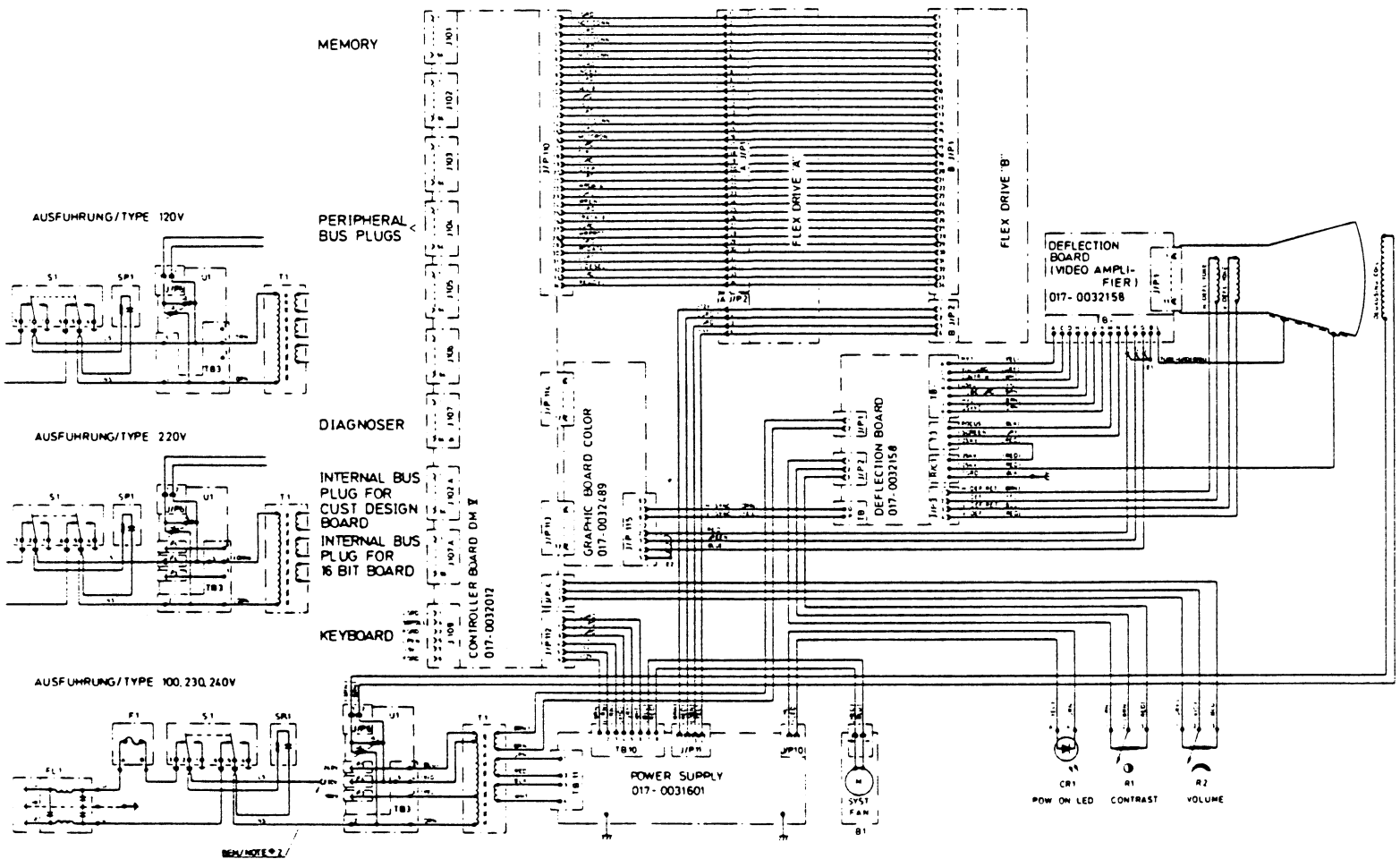
+5V Hybrid 017-0031624 Rev. D





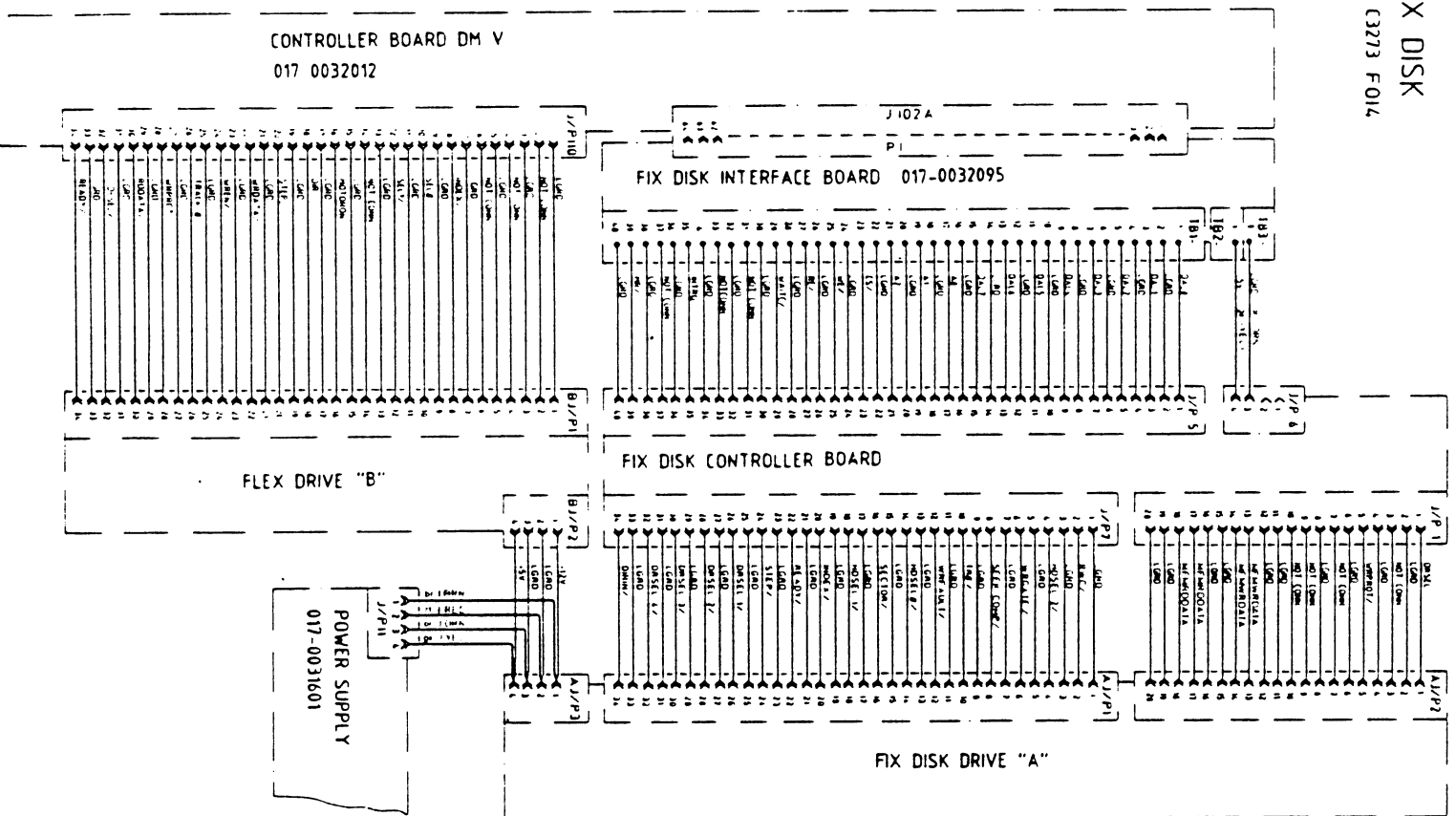
+12V Hybrid 017-0031944 Rev. D

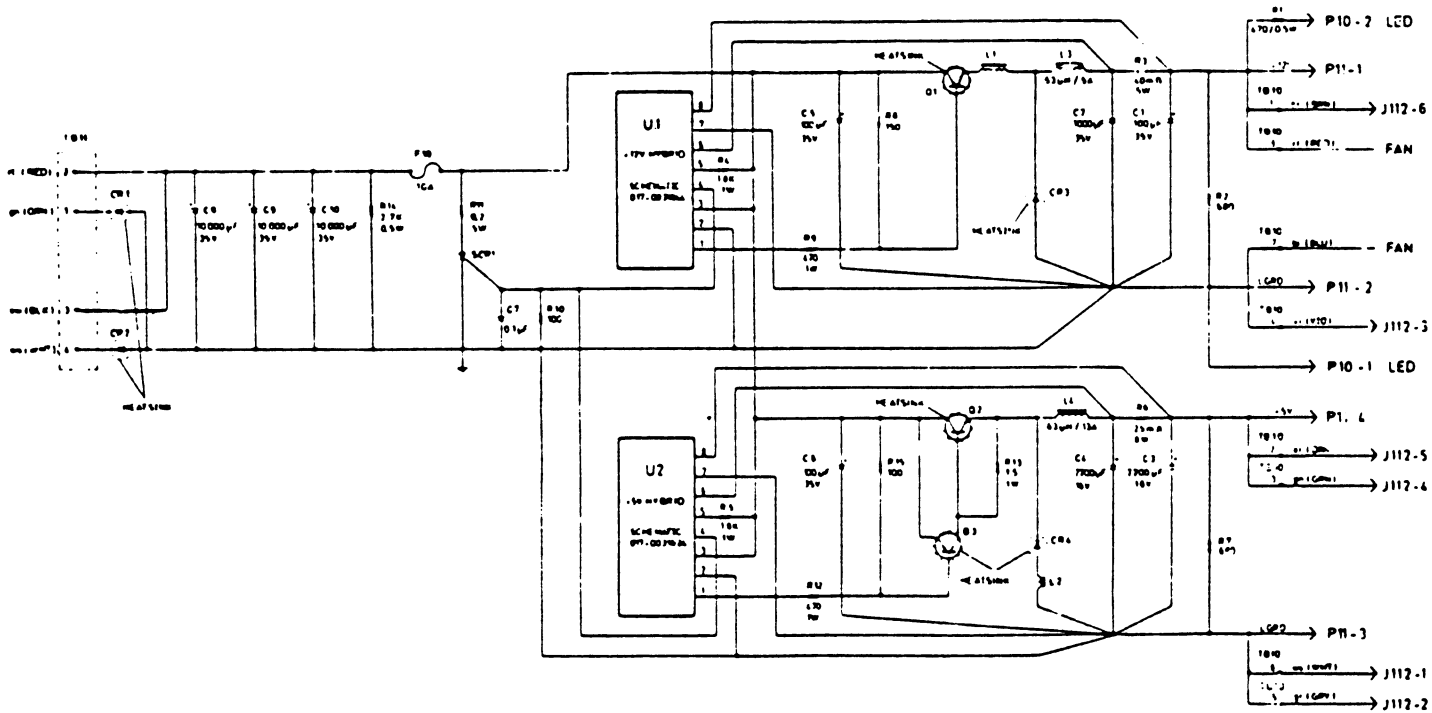




FIX DISK

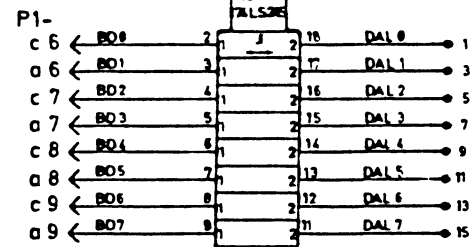
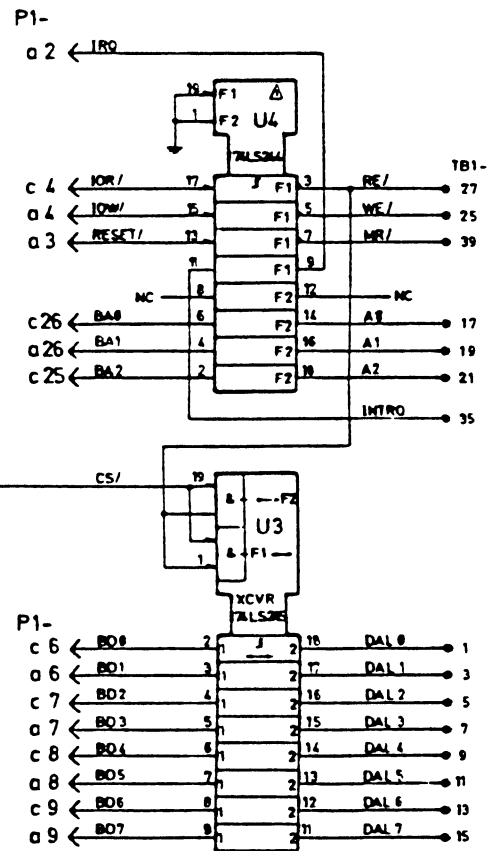
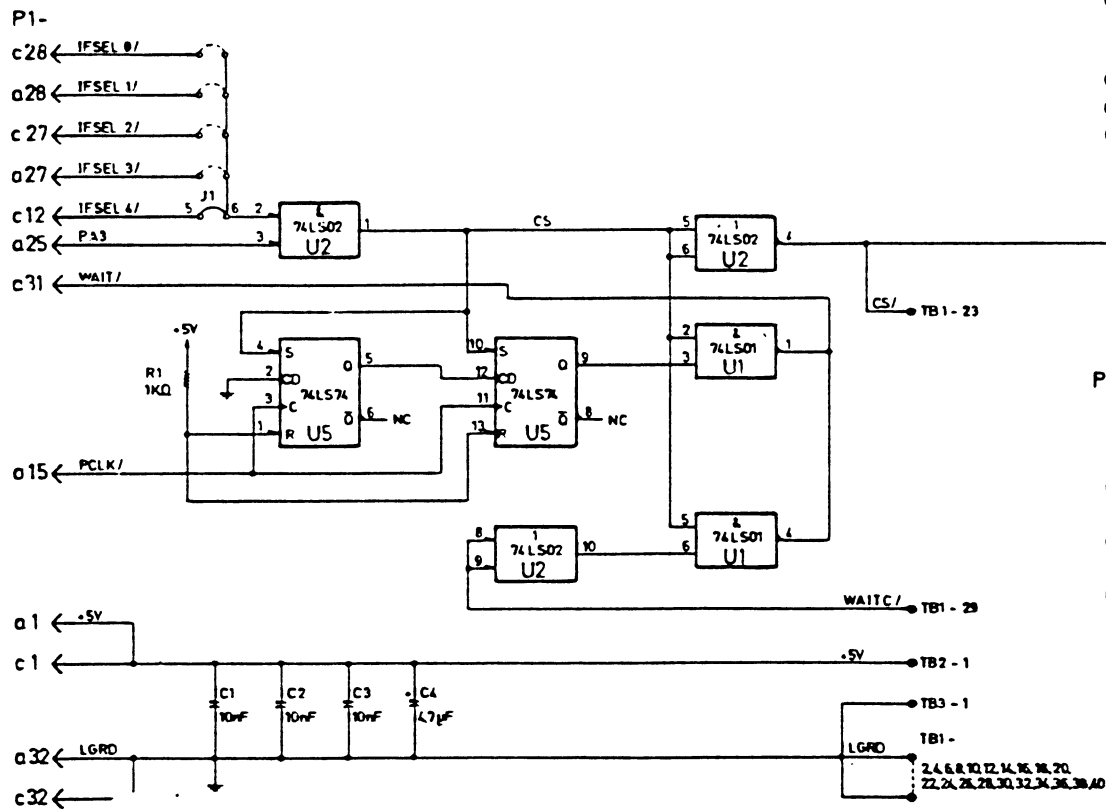
C3273 F014

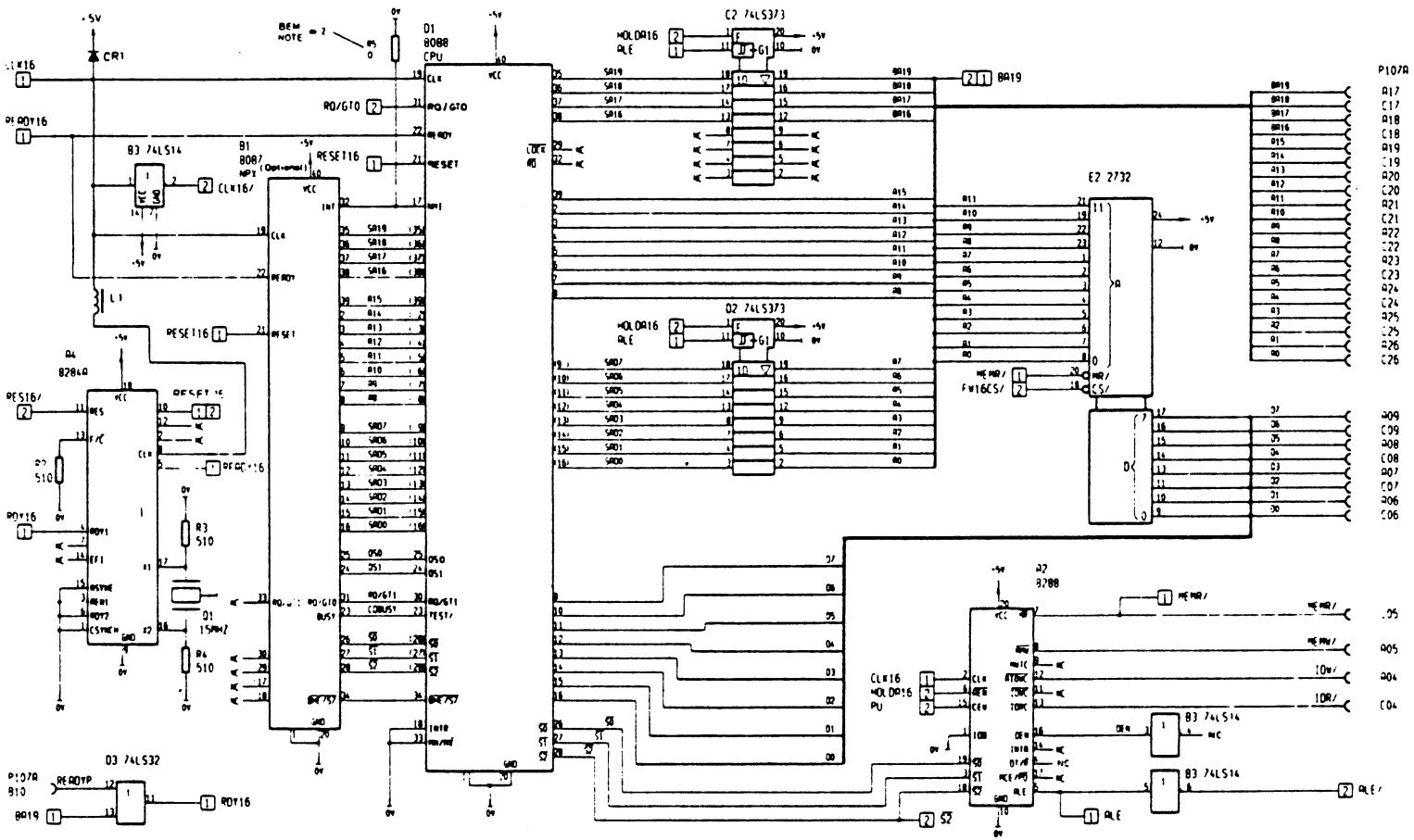


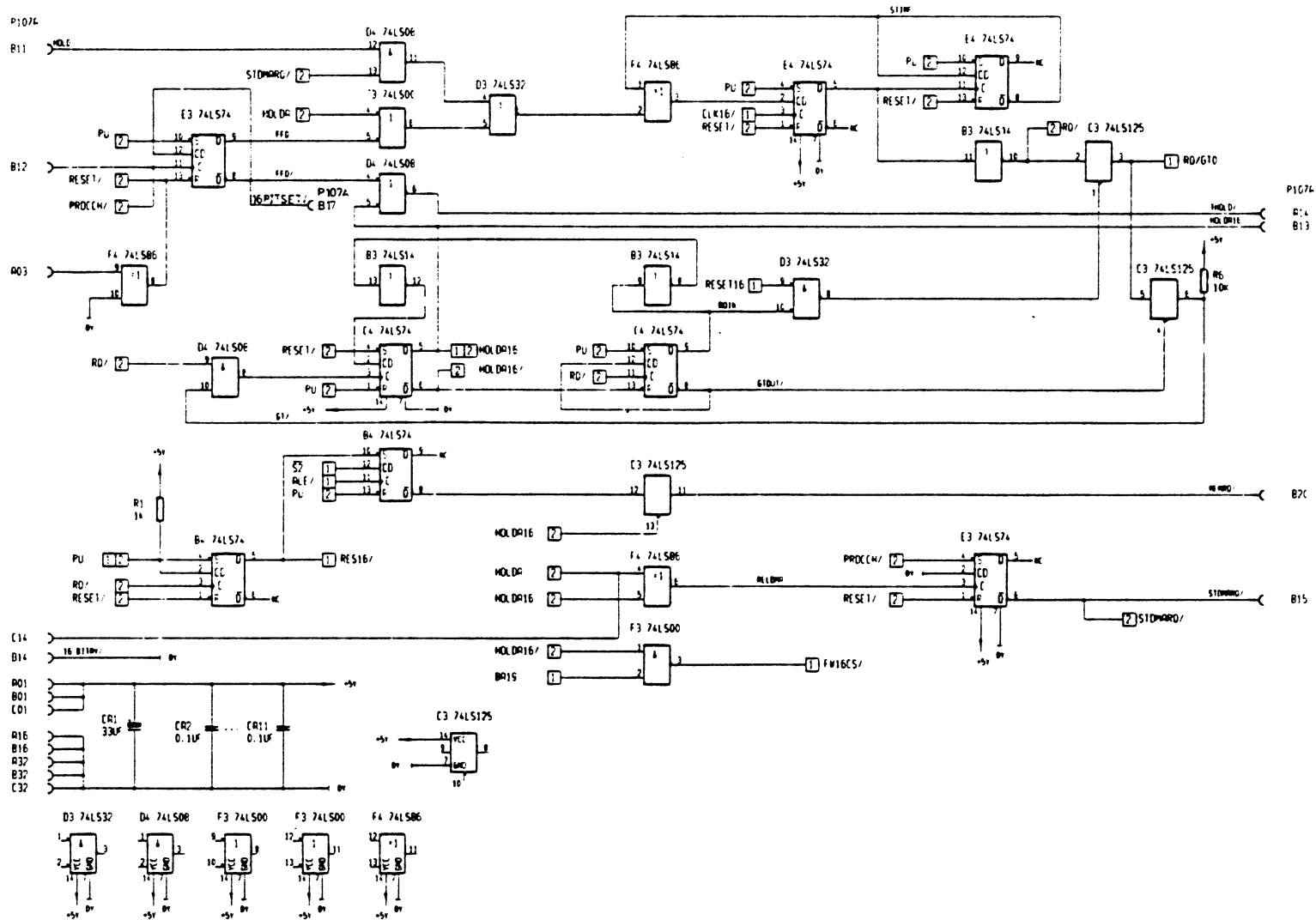


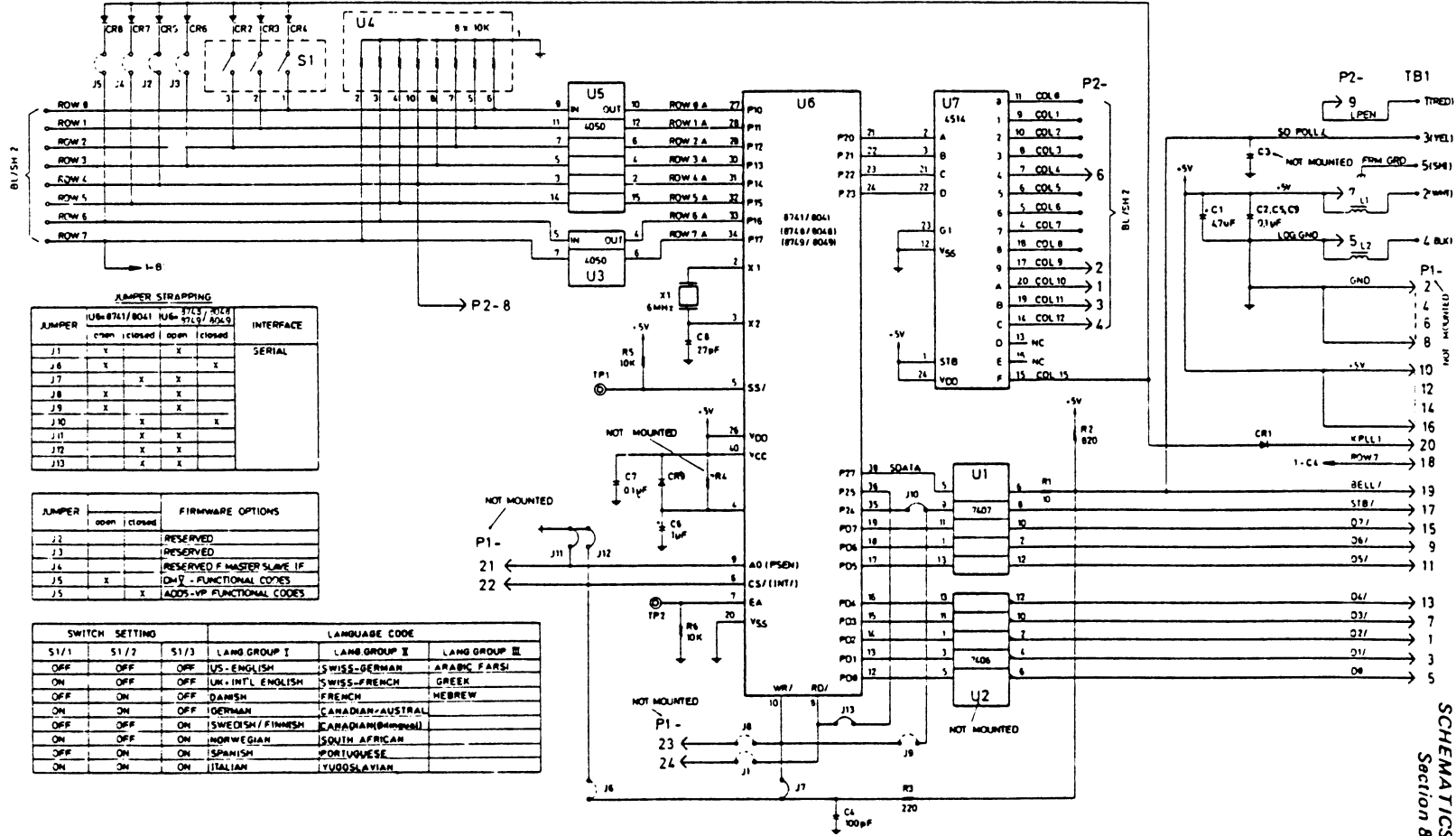
Power Supply 017-0031601 Rev. F

Integrated Fixed Disk Interface 017-0032095 Rev. B

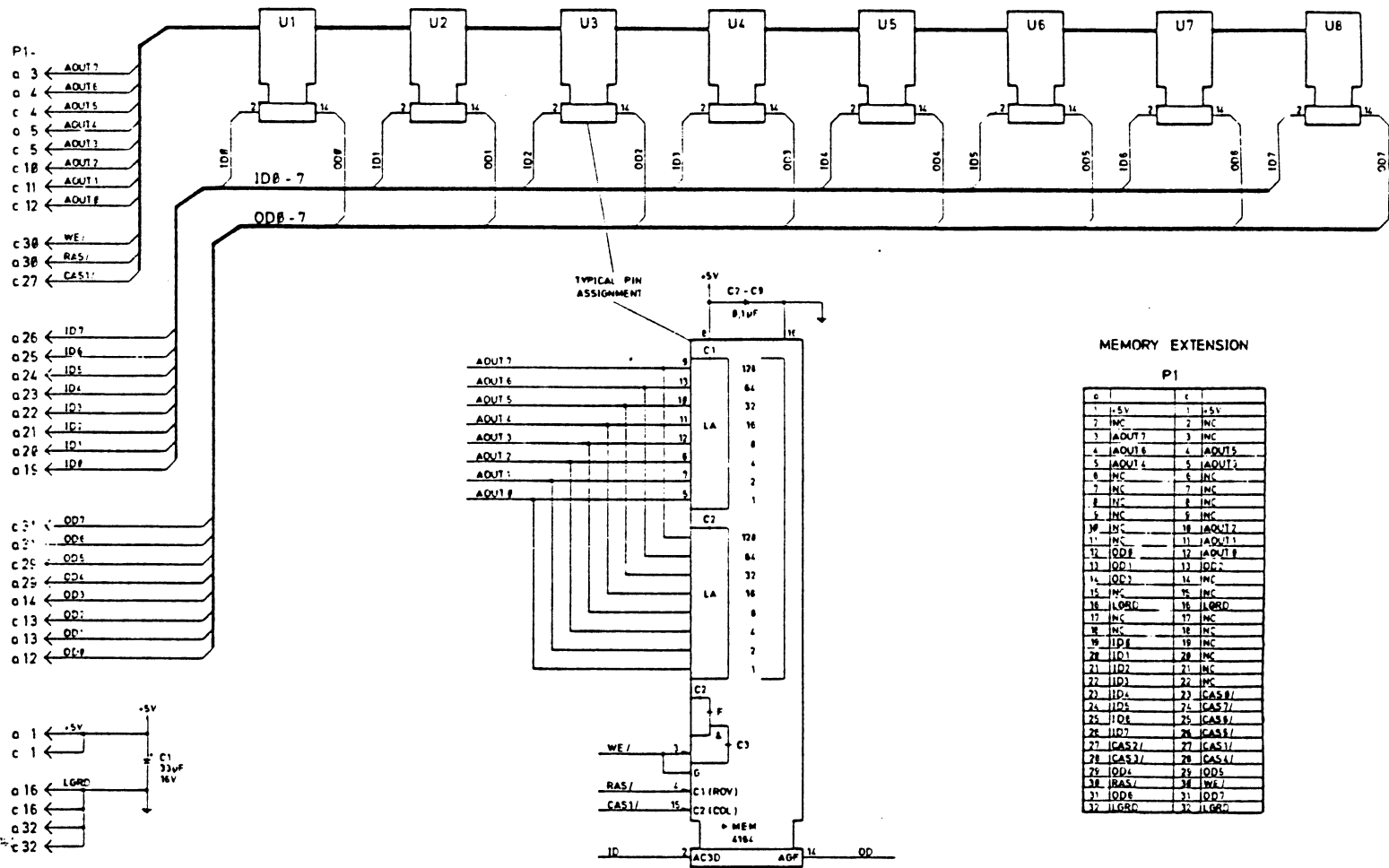


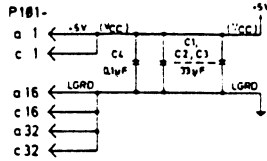
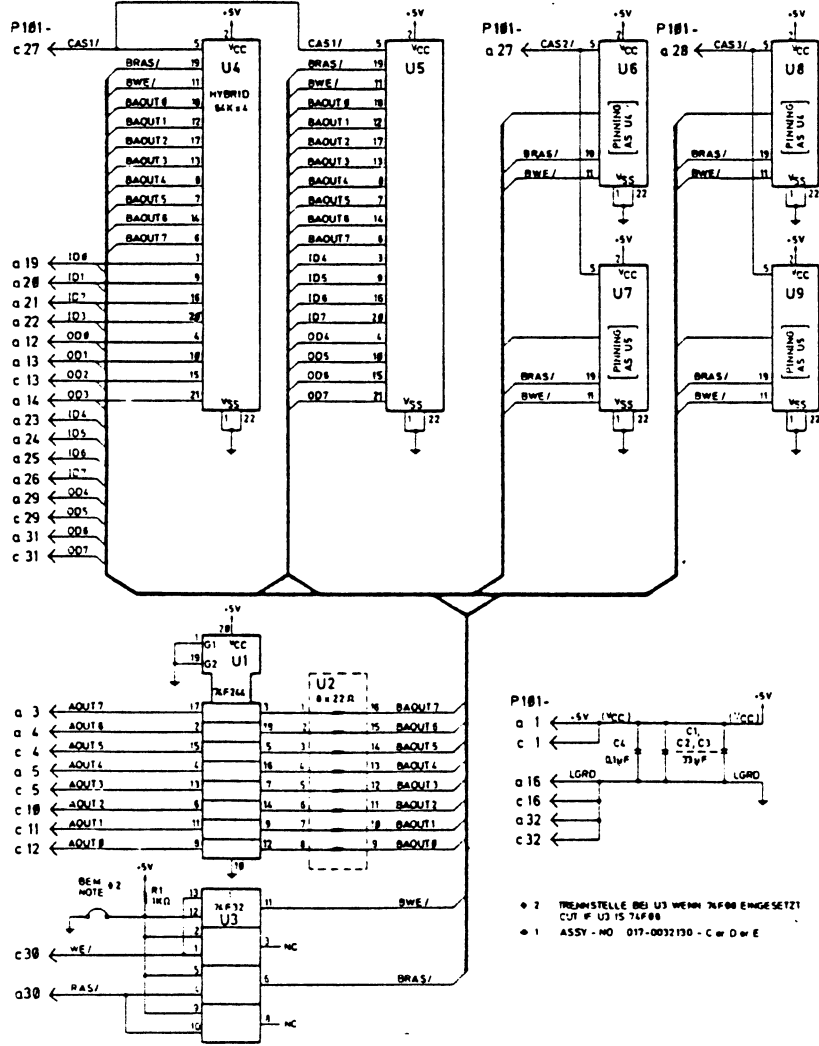






Memory Extension 64K (K200) 017-0032558 Rev. A



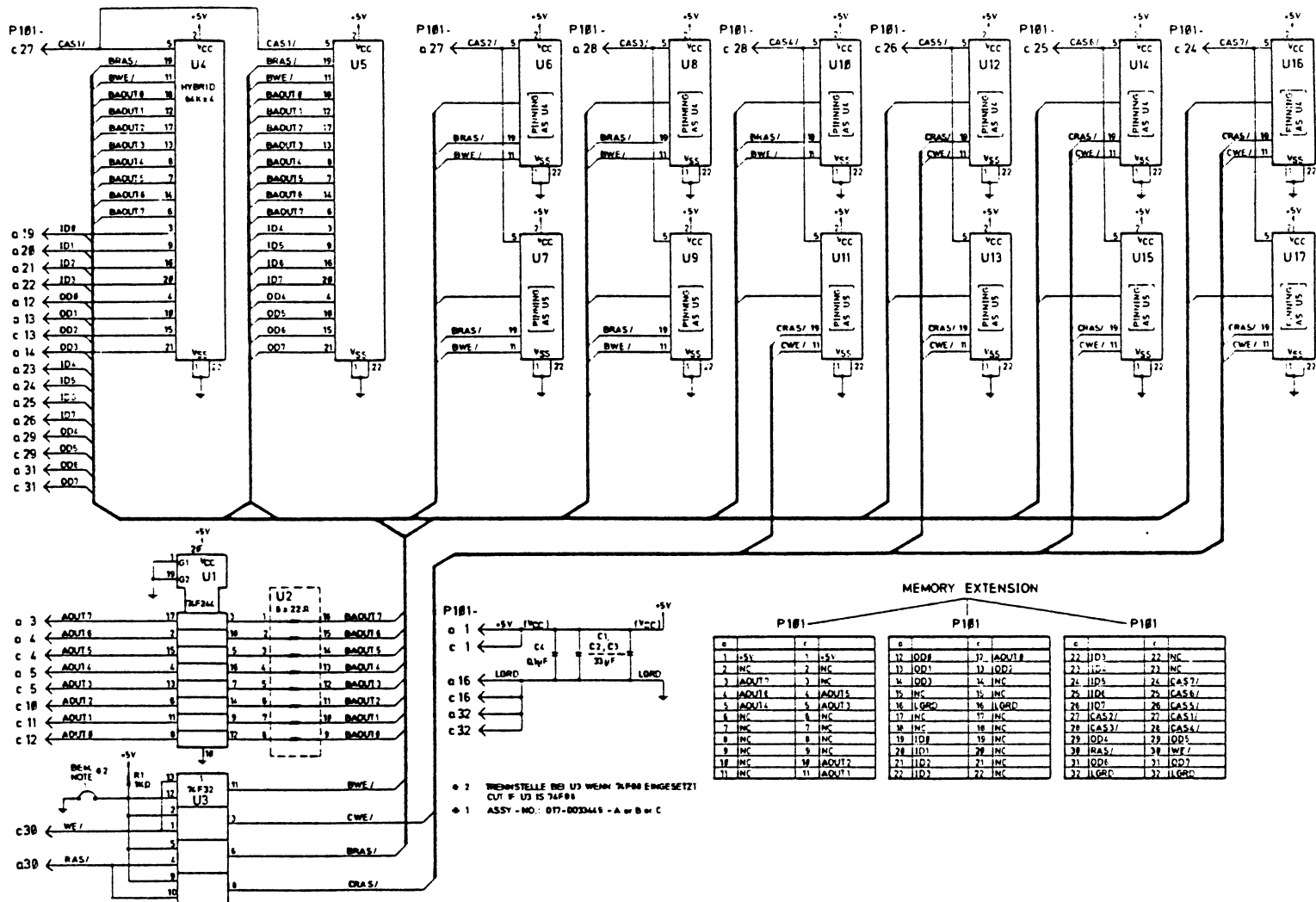


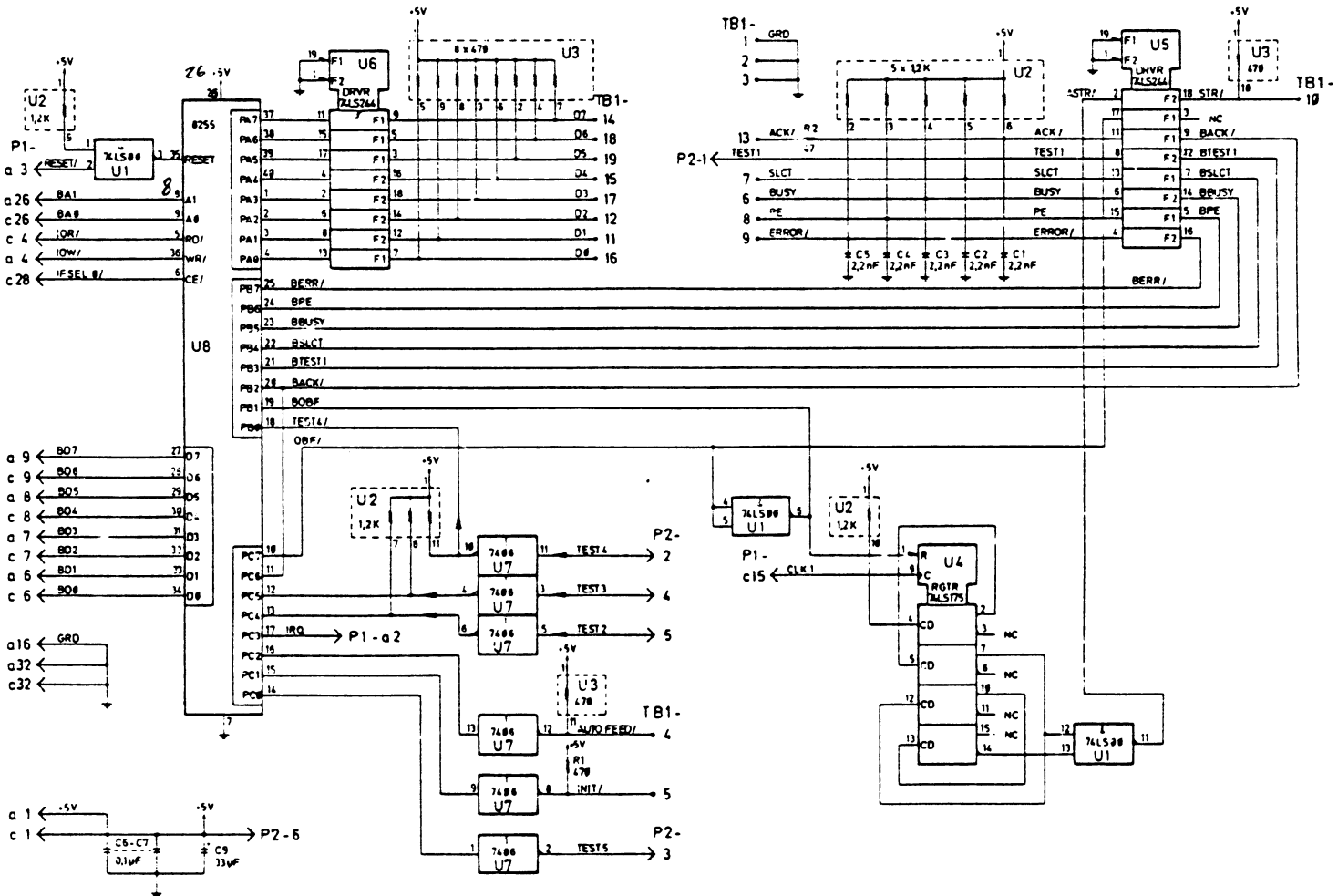
- 2 TRENNSTELLE BEI U3 WENN 76F00 EINGESETZT CUT # U3 IS 76F00
- 1 ASSY - MO 017-0032130 - C = D or E

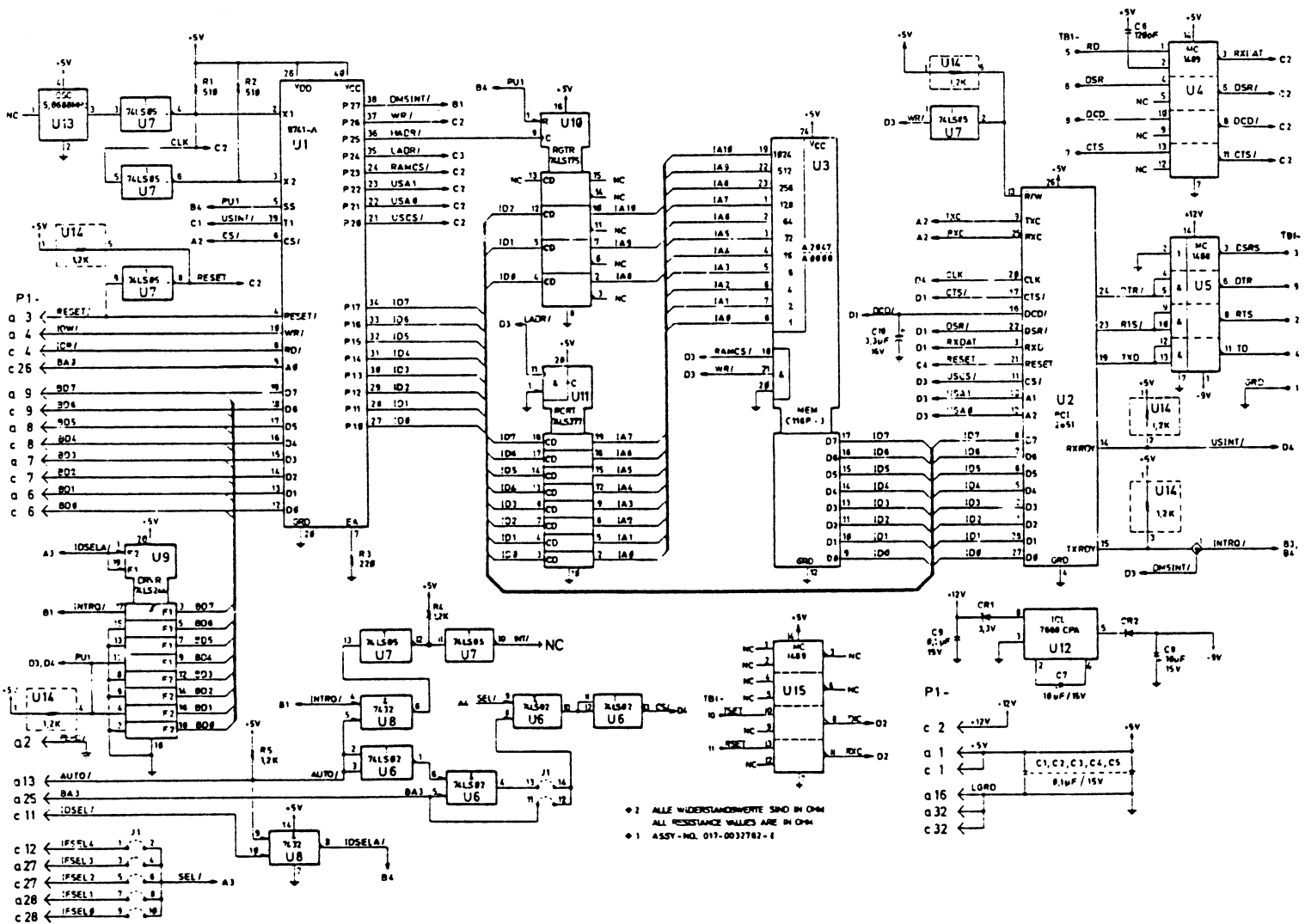
MEMORY EXTENSION

P101	P101	P101
1 1.5V	1 1.5V	1 1.5V
2 1.5V	2 1.5V	2 1.5V
3 1.5V	3 1.5V	3 1.5V
4 1.5V	4 1.5V	4 1.5V
5 1.5V	5 1.5V	5 1.5V
6 1.5V	6 1.5V	6 1.5V
7 1.5V	7 1.5V	7 1.5V
8 1.5V	8 1.5V	8 1.5V
9 1.5V	9 1.5V	9 1.5V
10 1.5V	10 1.5V	10 1.5V
11 1.5V	11 1.5V	11 1.5V
12 1.5V	12 1.5V	12 1.5V
13 1.5V	13 1.5V	13 1.5V
14 1.5V	14 1.5V	14 1.5V
15 1.5V	15 1.5V	15 1.5V
16 1.5V	16 1.5V	16 1.5V
17 1.5V	17 1.5V	17 1.5V
18 1.5V	18 1.5V	18 1.5V
19 1.5V	19 1.5V	19 1.5V
20 1.5V	20 1.5V	20 1.5V
21 1.5V	21 1.5V	21 1.5V
22 1.5V	22 1.5V	22 1.5V

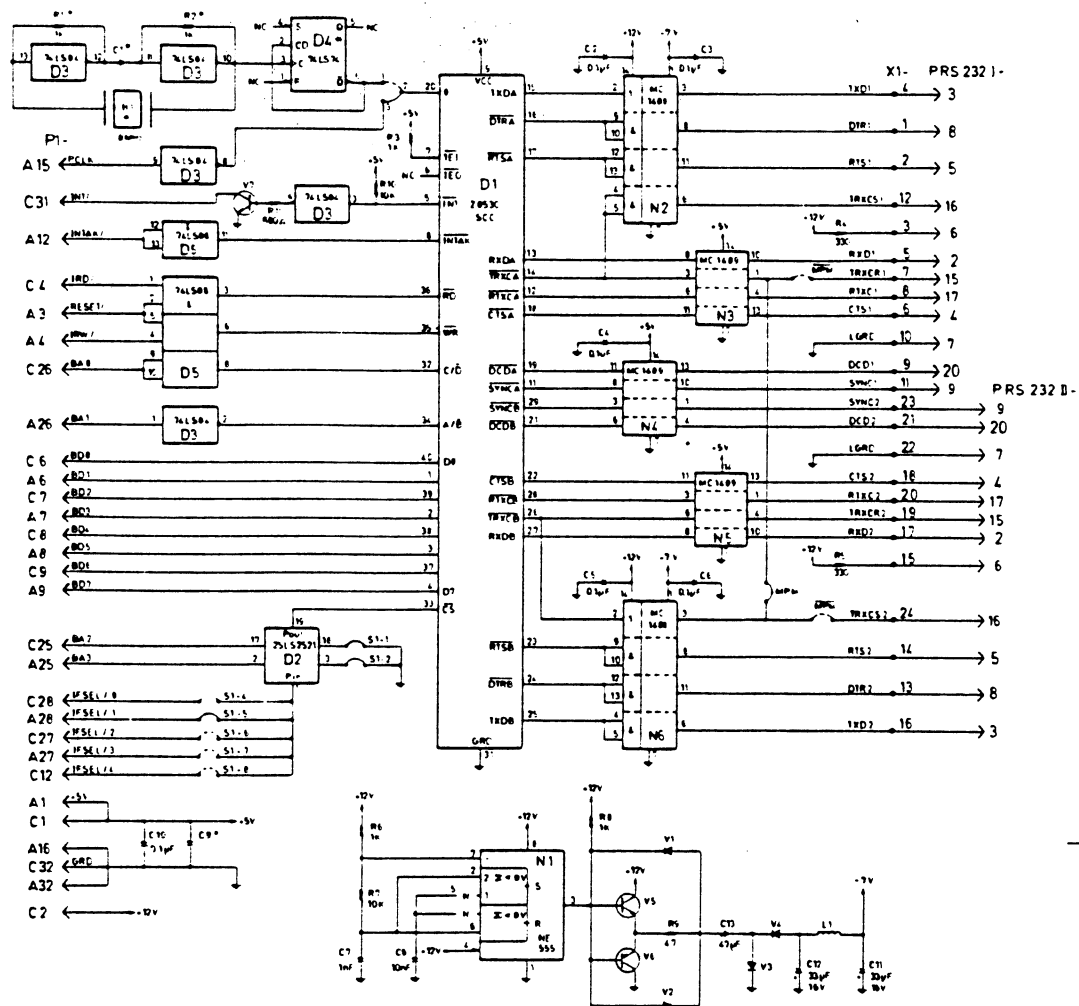
Memory Extension 448 K (K208) 017-003524 Rev. B



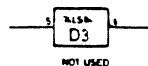


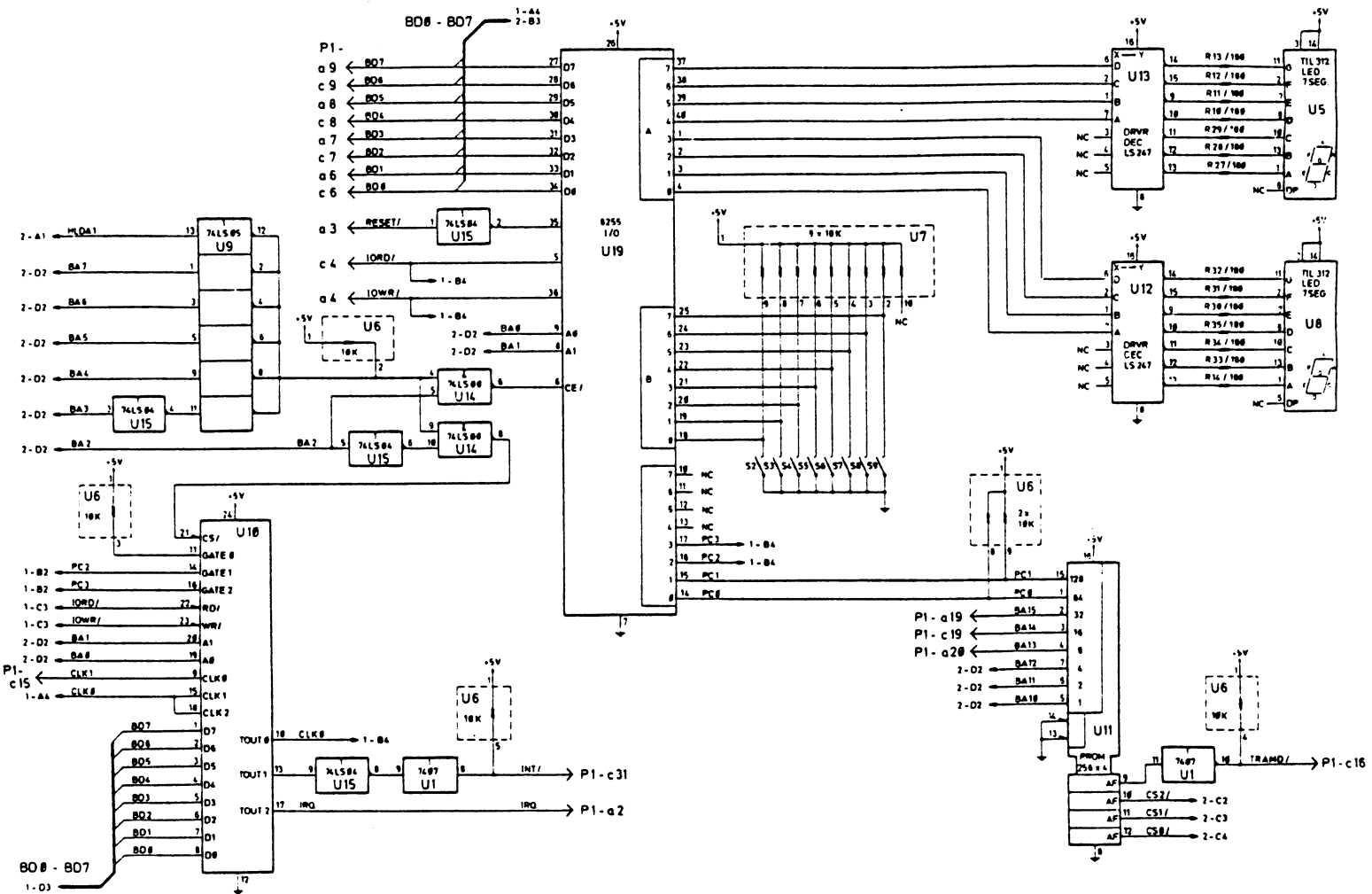


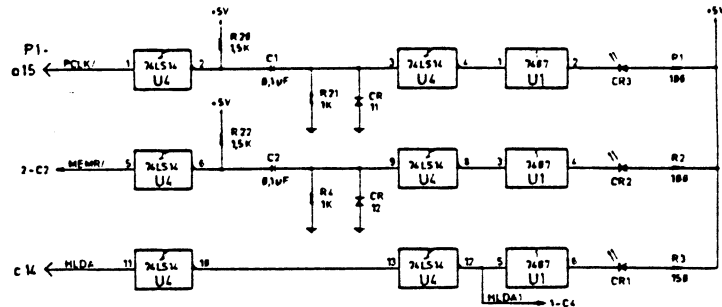
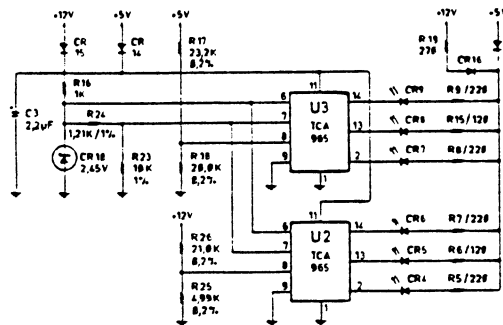
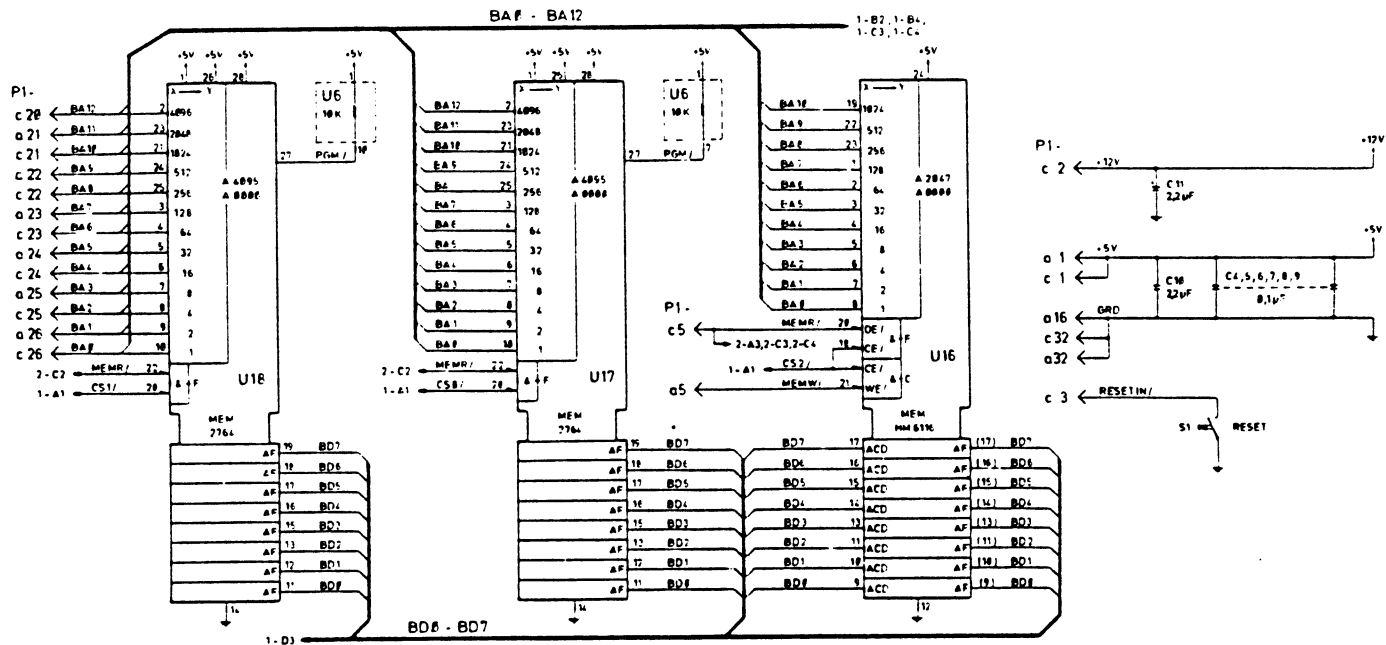
Buffered Async/Bisync Interface (K215) 017-0032792 Rev. C

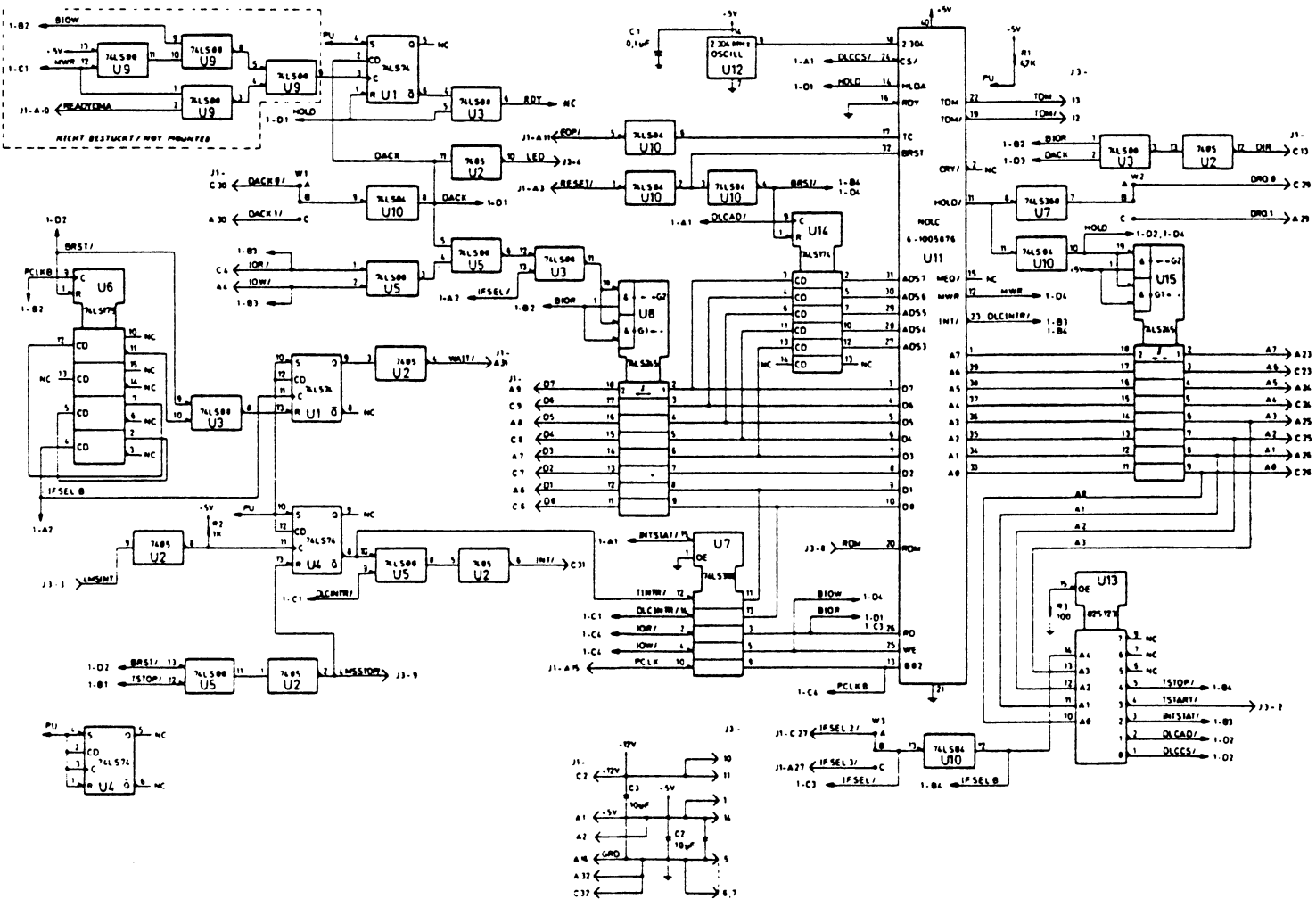


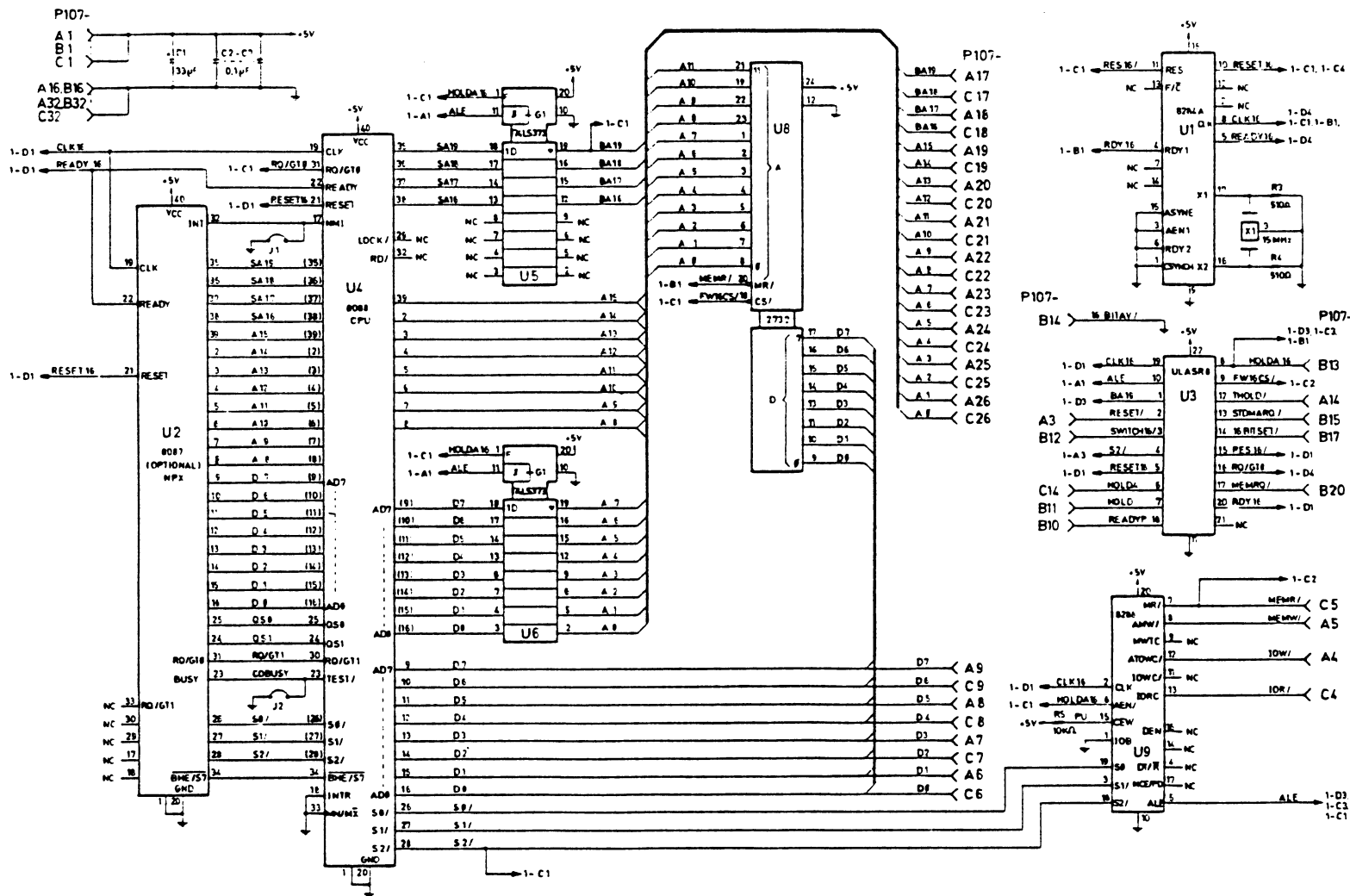
03 • NIKHIT BESHUKI
• NO.1 MOUNTIC
02 ALL WIDE ISLANDS WERE 70% SING IN LOW
ALL RESISTANT VILUF: ARE IN LOW
01 ASSY-NO 017-0033583.

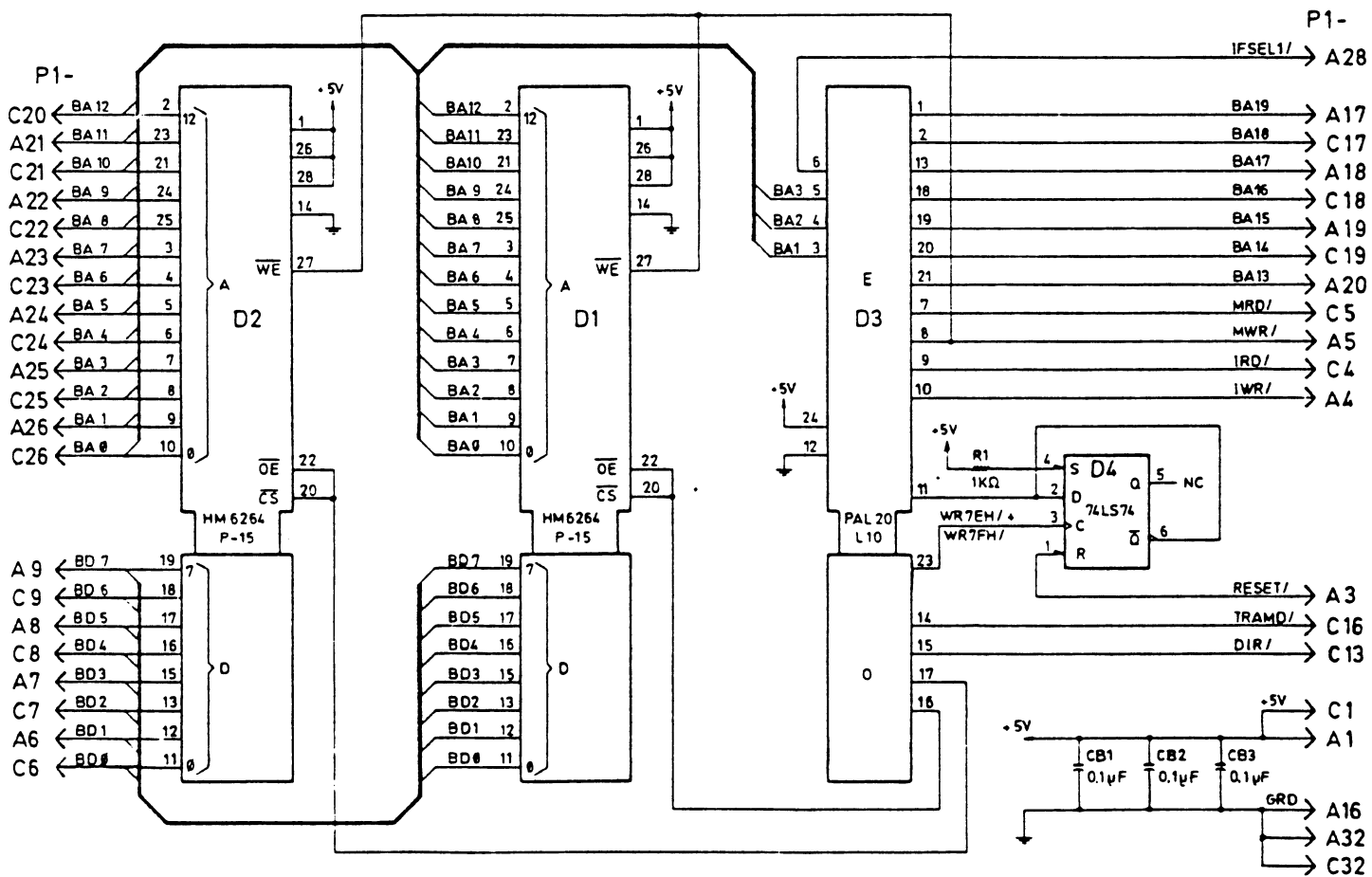


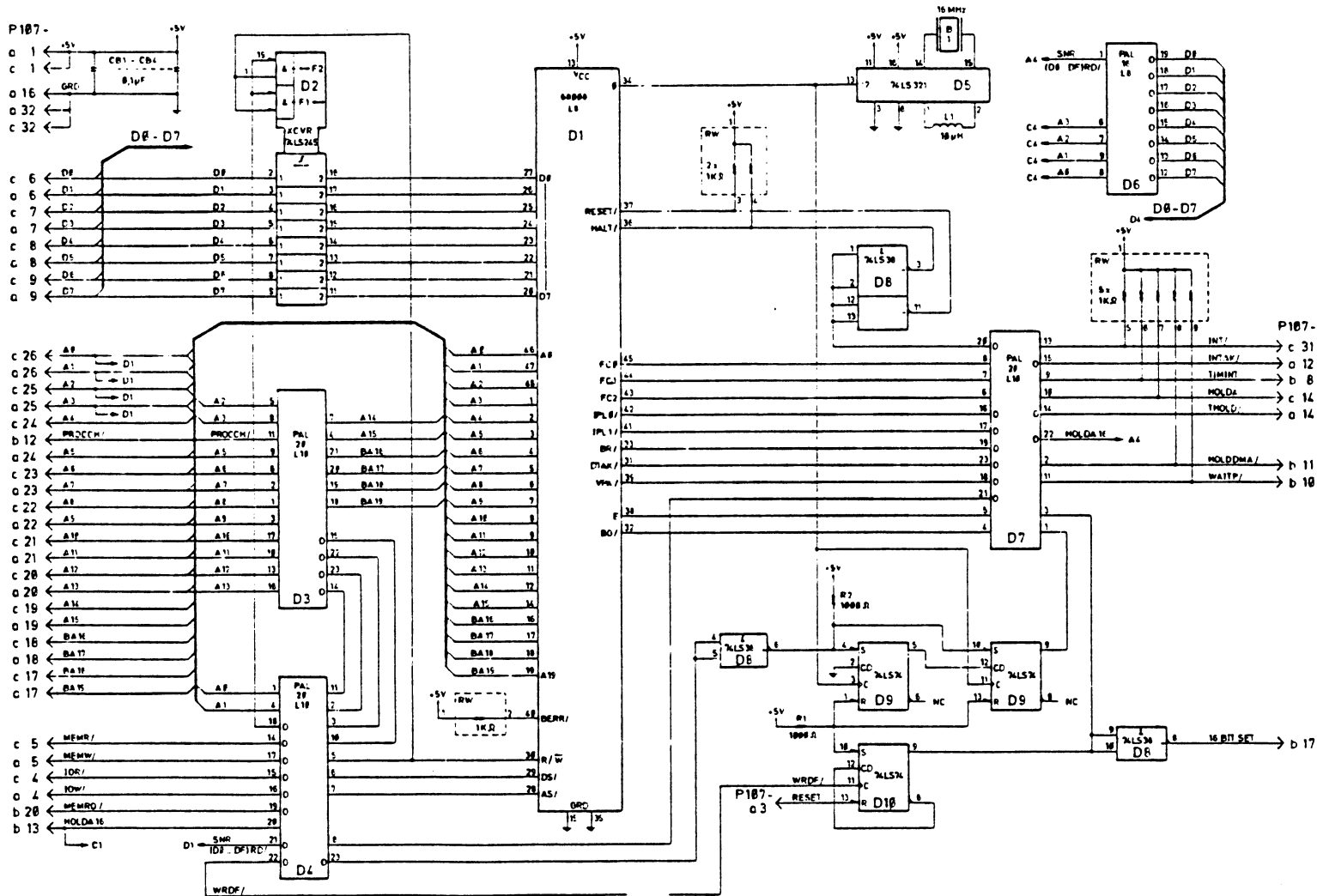


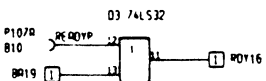






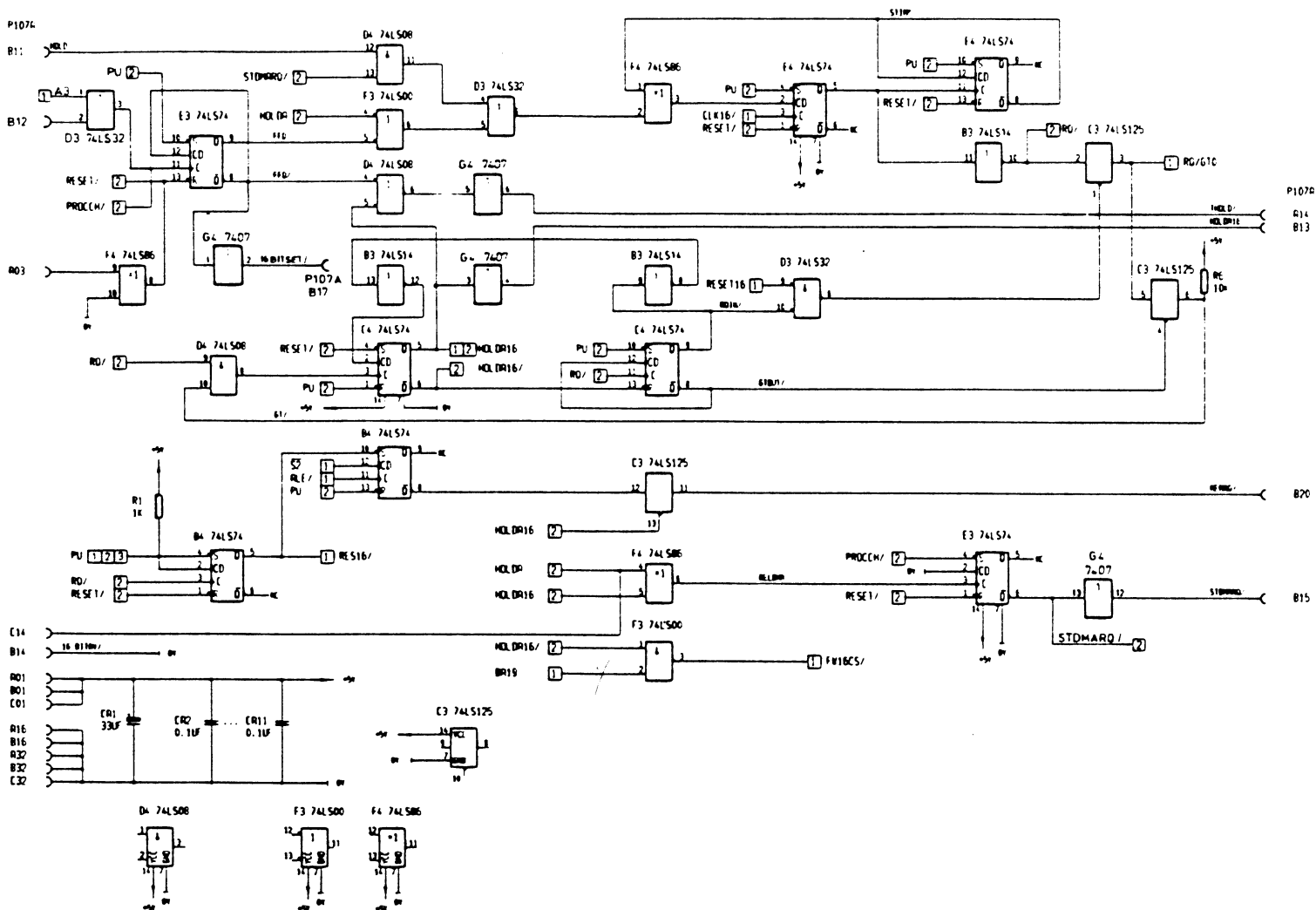




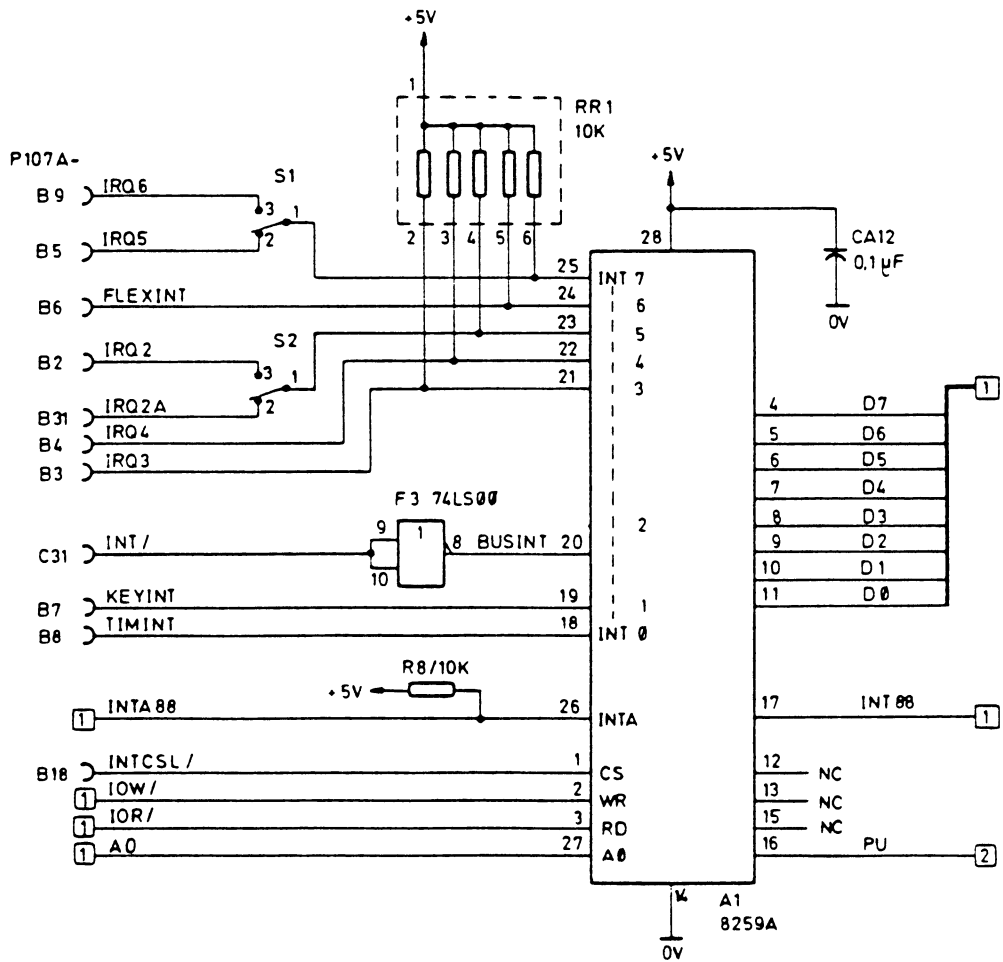


BEM /NOTE • 1 ASSY 017.00336^1. A

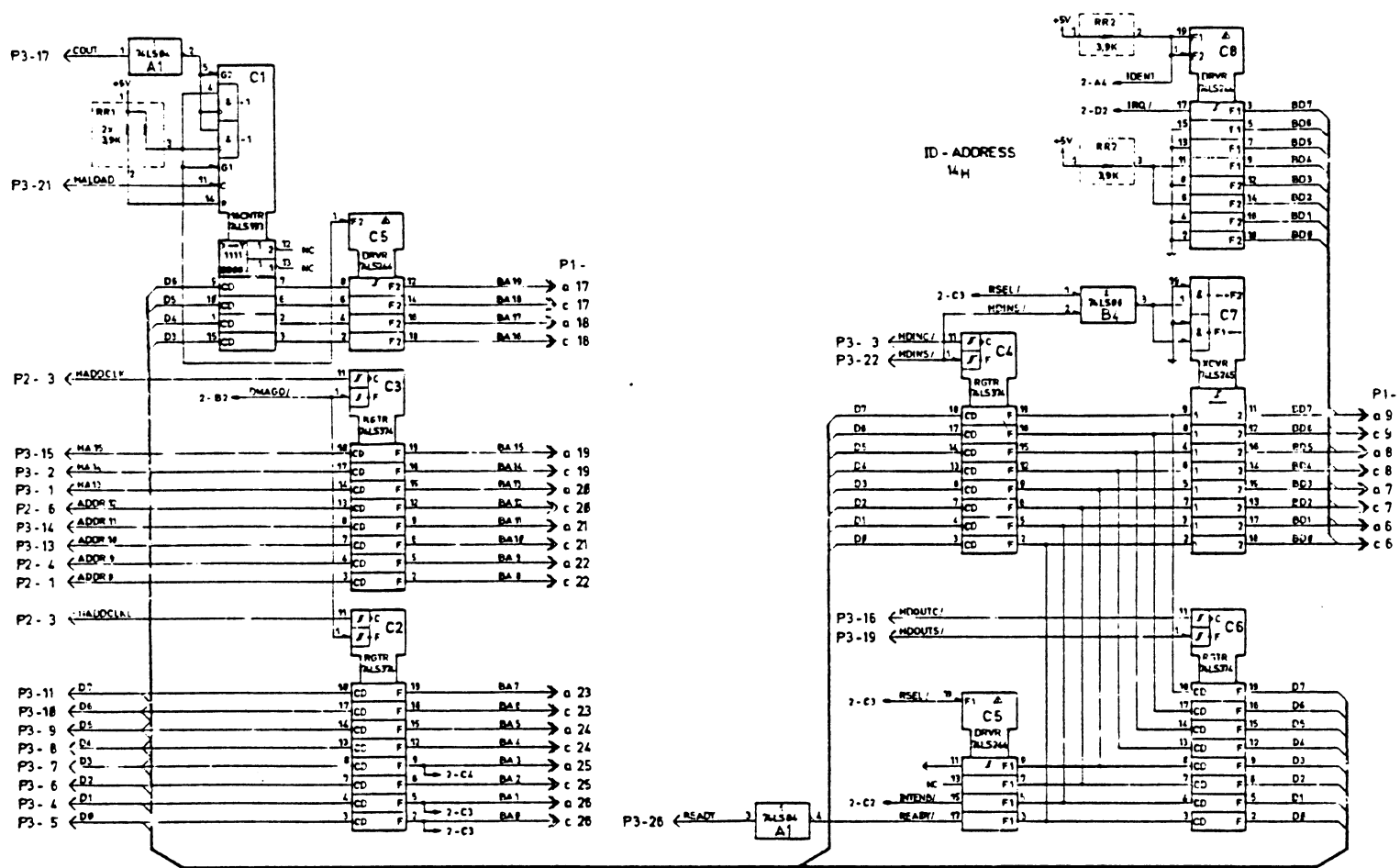
1 of 3



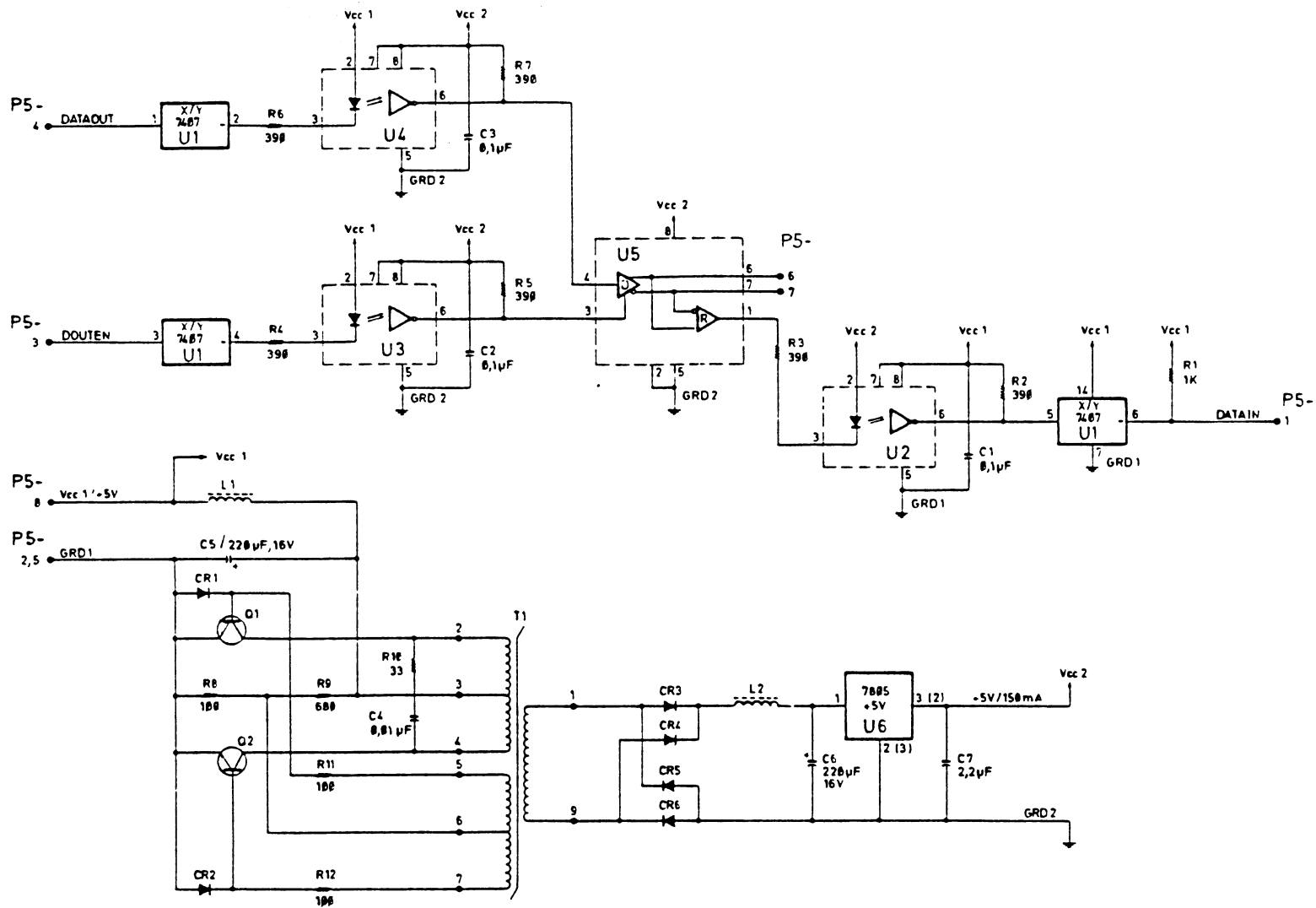
PIC

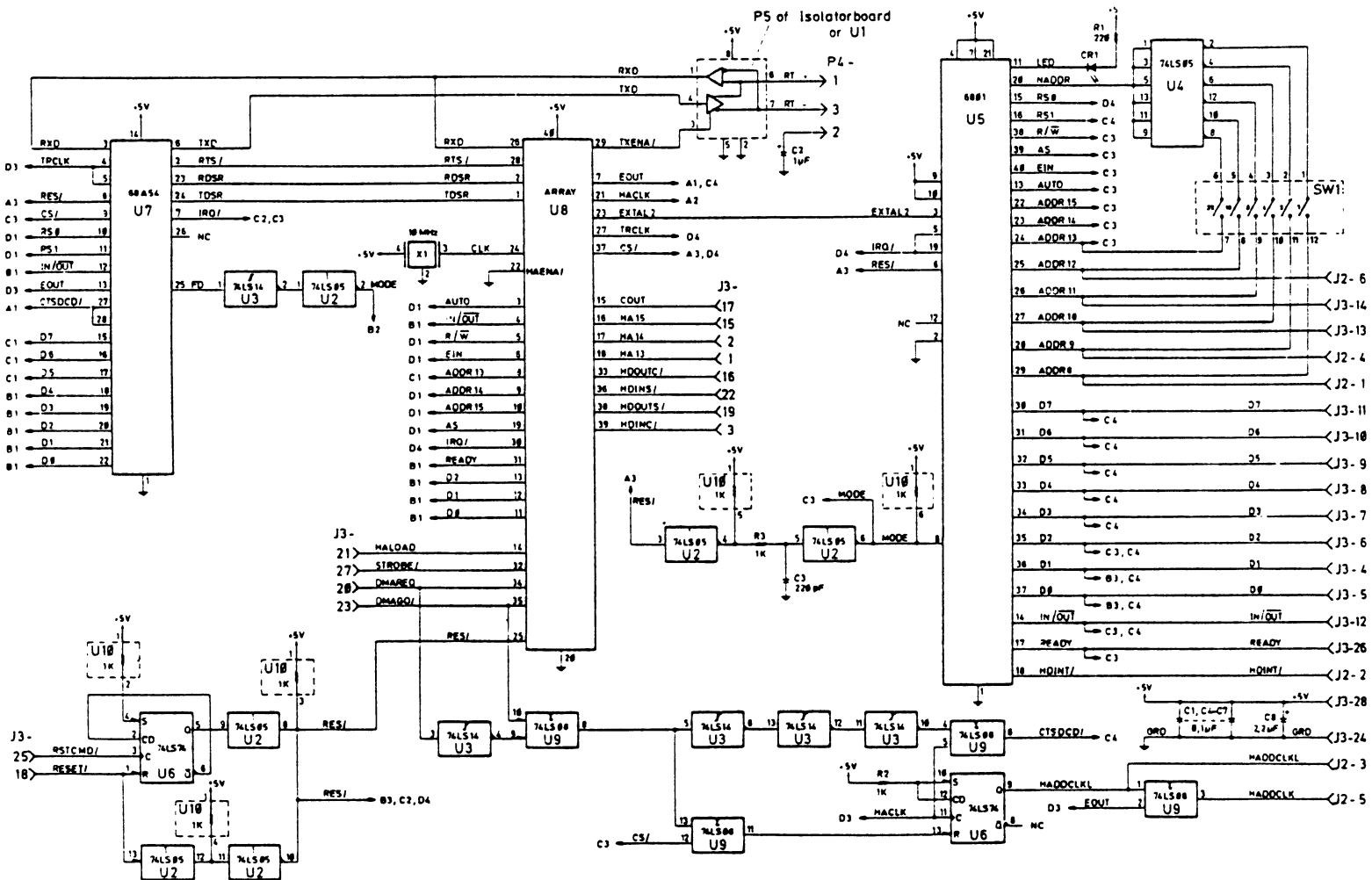


Omninet Interface (K600) 017-0032433 Rev. C
1 of 2



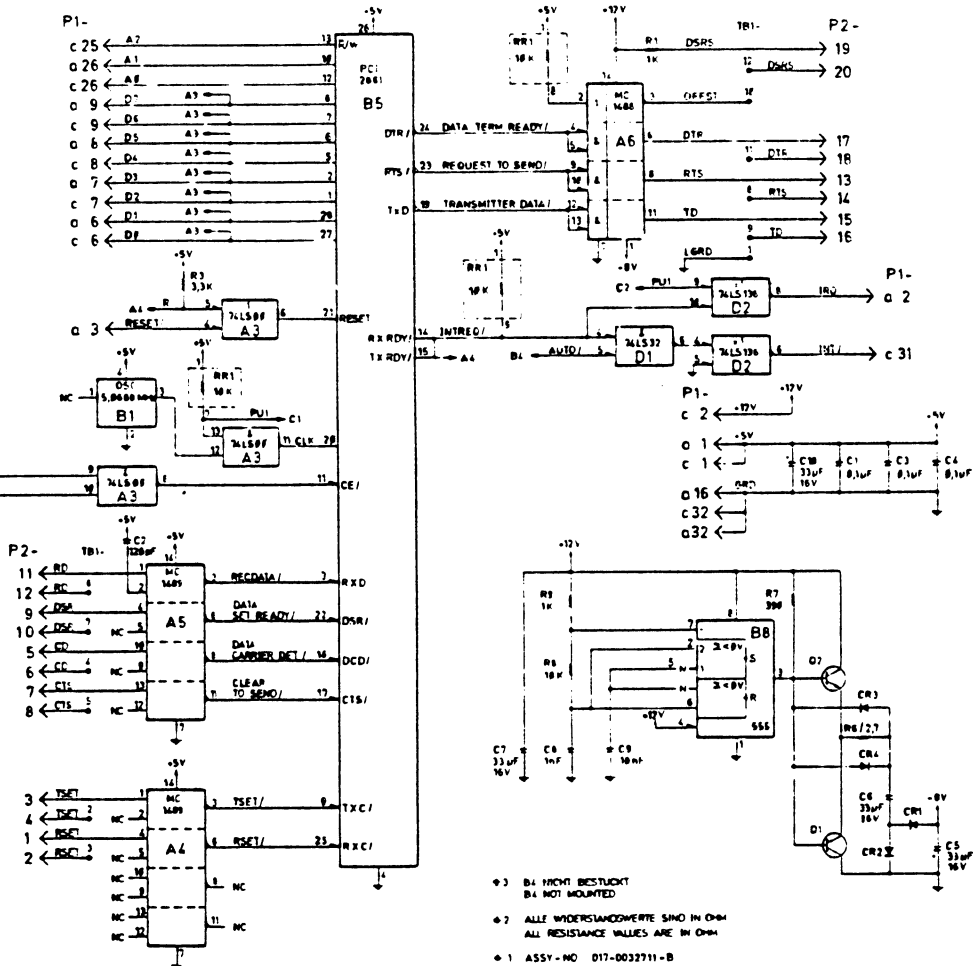
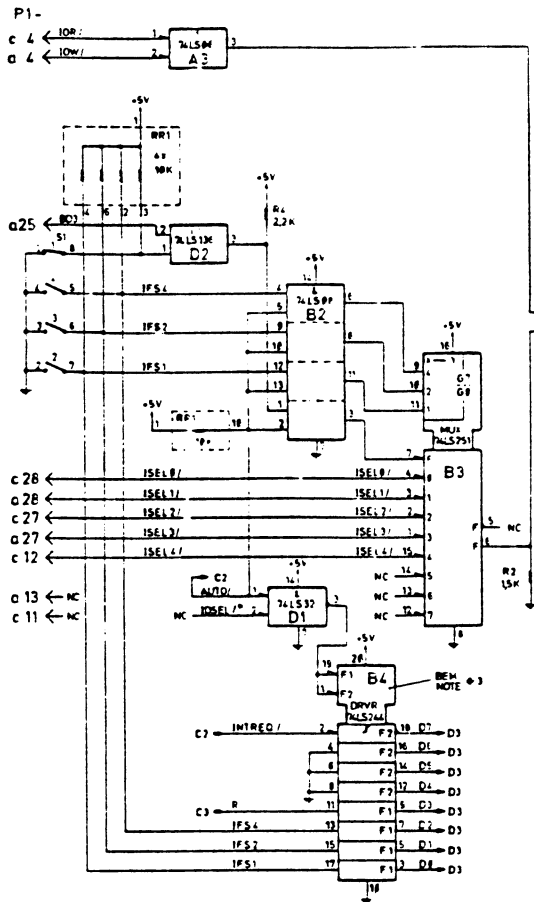






Omninet Controller (K600) 017-0032434 Rev. B

RS-232 C Switchable Interface (K801) 017-0032712 Rev. B

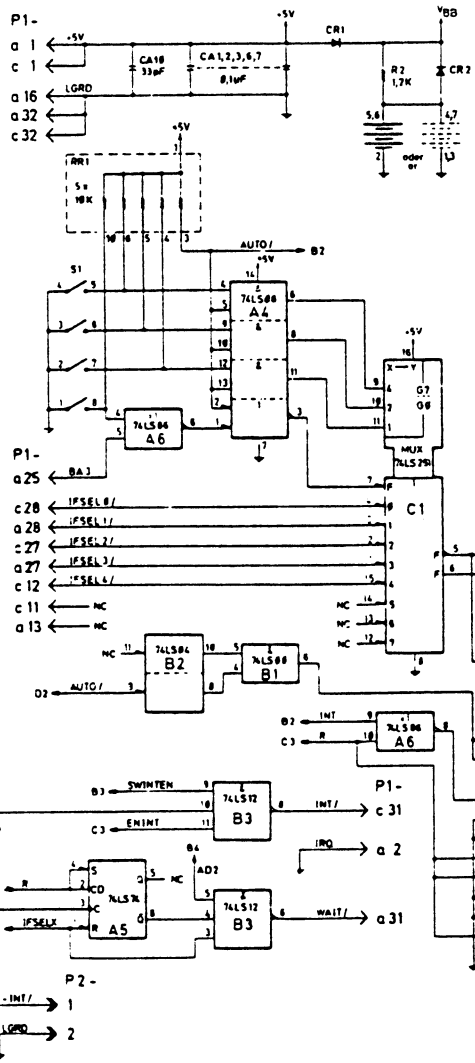
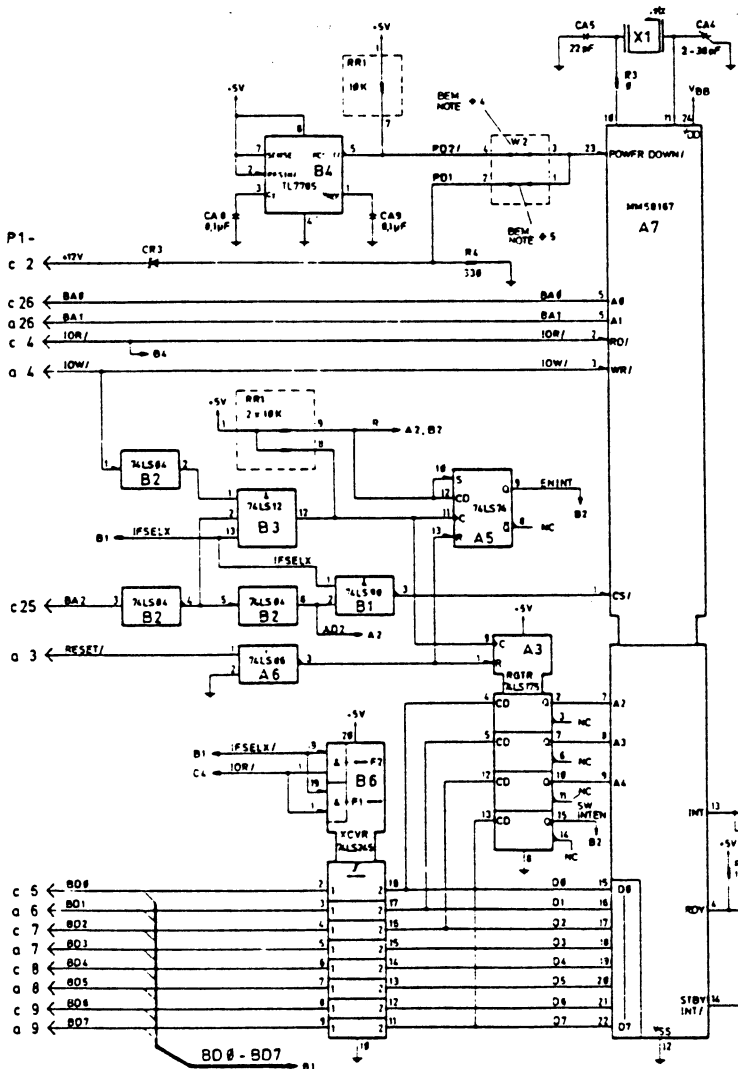


- 3 B4: NICHT BESTUKT
B4: NOT MOUNTED
- 7 ALLE WIDERSTANDSWERTE SIND IN OHM
ALL RESISTANCE VALUES ARE IN OHM
- 1 ASSY.-NO. 017-0032711-B

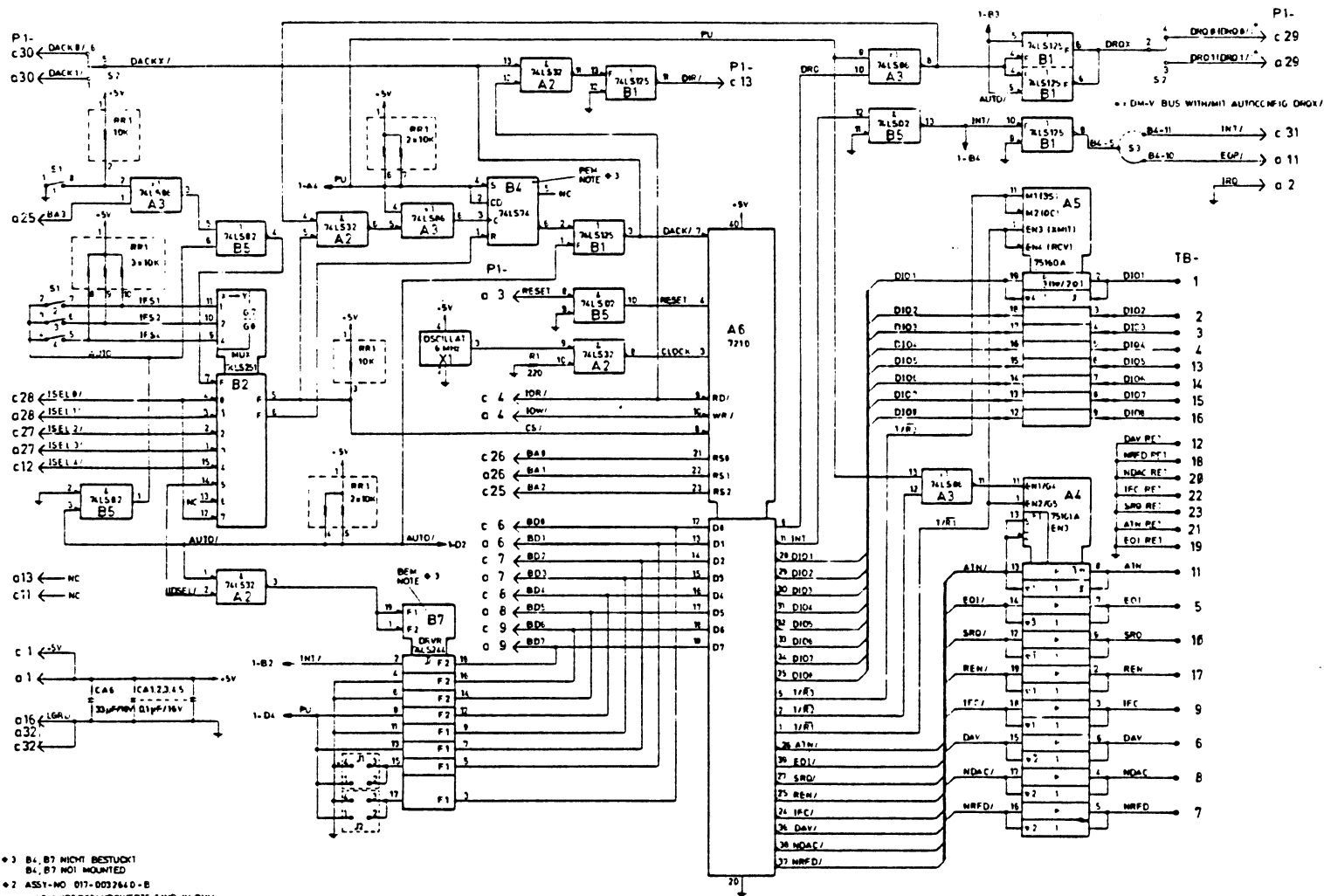
ADR. C8 - CF

- *8 B1, CA8, CA9, B5 NICHT BESTÜCKT
B1, CA8, CA9, B5 NOT MOUNTED
- *5 W2/1 NACH W2/2 VERBUNDEN
W2/1 TO W2/2 CONNECTION
- *4 W2/3 NACH W2/4 UNTERGESCHNITTEN
W2/3 TO W2/4 CUT
- *3 W1 NICHT BESTÜCKT
W1 NOT MOUNTED
- *2 ALLE WIDERSTANDSWERTE SIND IN OHM
ALL RESISTANCE VALUES ARE IN OHM
- *1 ASSY-NO: 017-0032703-B

Real Time Clock (K803) 017-0032703 Rev. B



IEEE-488 Interface (K804) 017-0032641 Rev. B



- *3 B4, B7 NOT BESTOCKT
 B4, B7 NOT MOUNTED
 *2 ASSY-NO. 017-0032640-B
 *1 ALLE WIDERSTANDSWERTE SIND IN OHM
 ALL RESISTANCE VALUES ARE IN OHM

