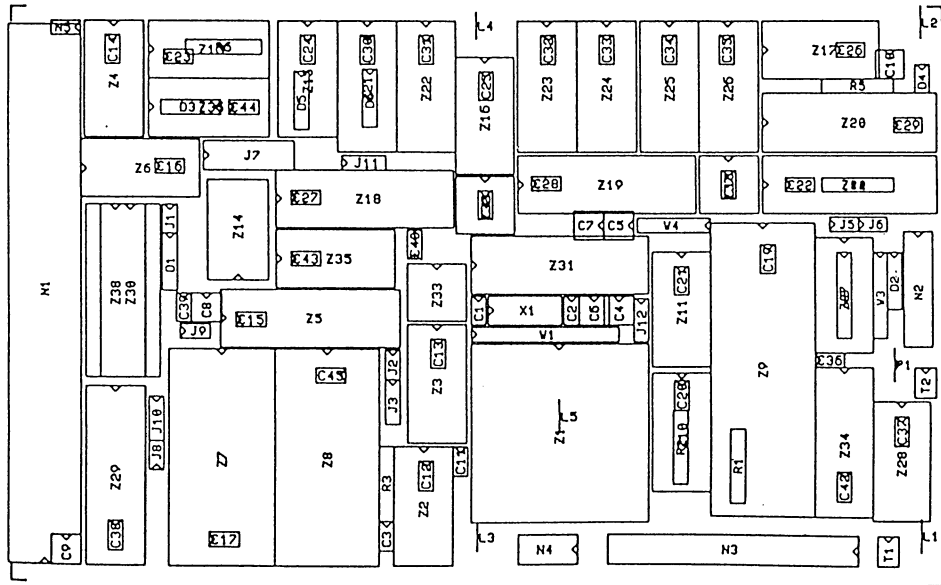
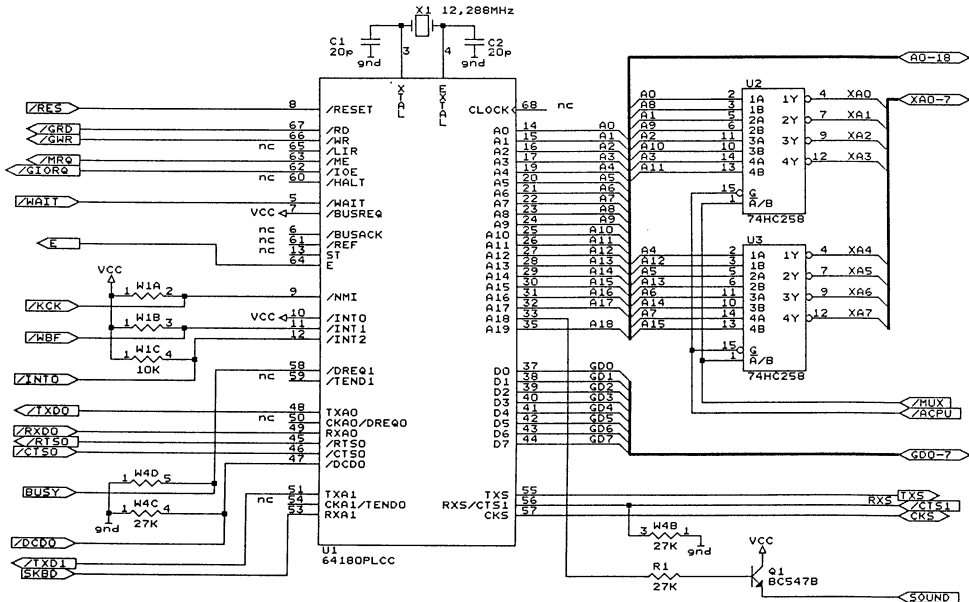


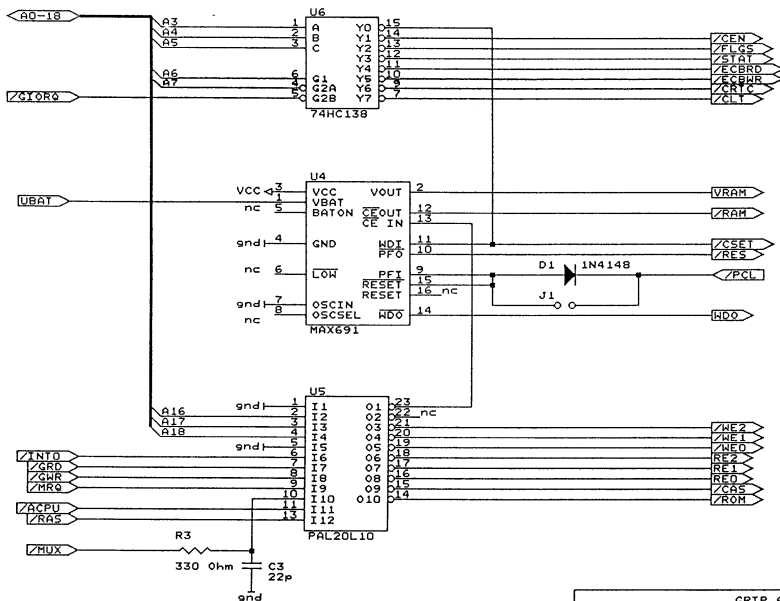
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②

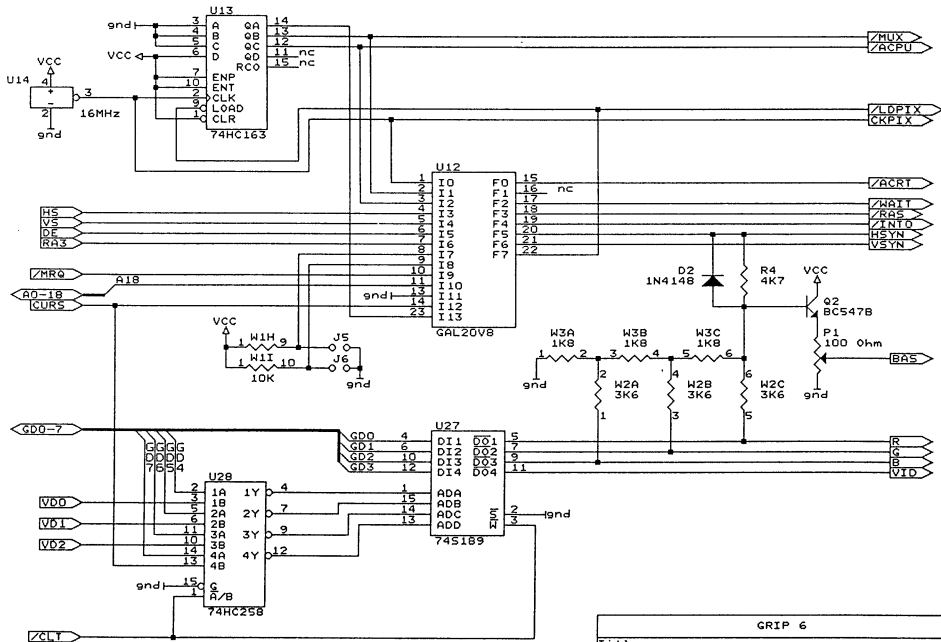




GRIP 6			
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CPU			
Size	Document Number	REV	
A	Conitec JCL	0	
Date:	August 9, 1990	Sheet	1 of 12



GRIP 6			
Title			
Decoder			
Size	Document Number	REV	
A	Conitec JCL	0	
Date:	August 9, 1990	Sheet	2 of 12



GRIP 6

Title

Timer/Video

Size Document Number

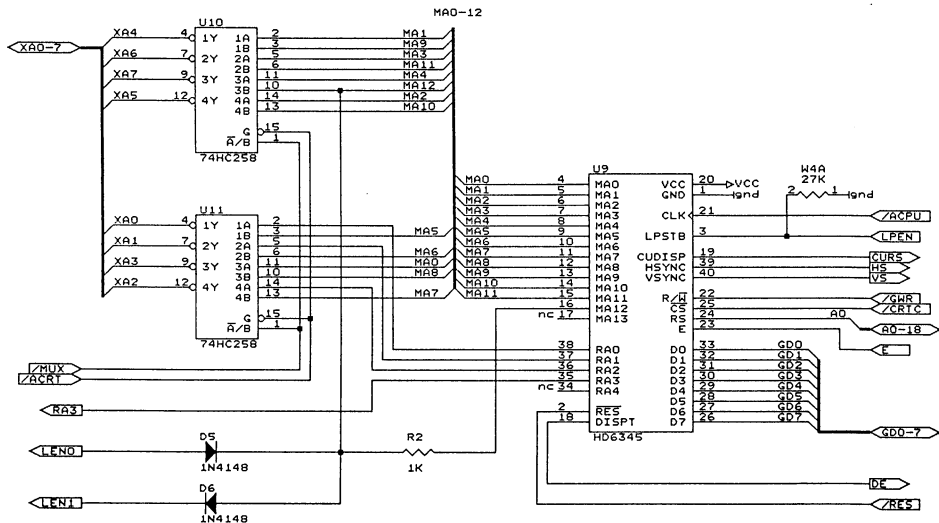
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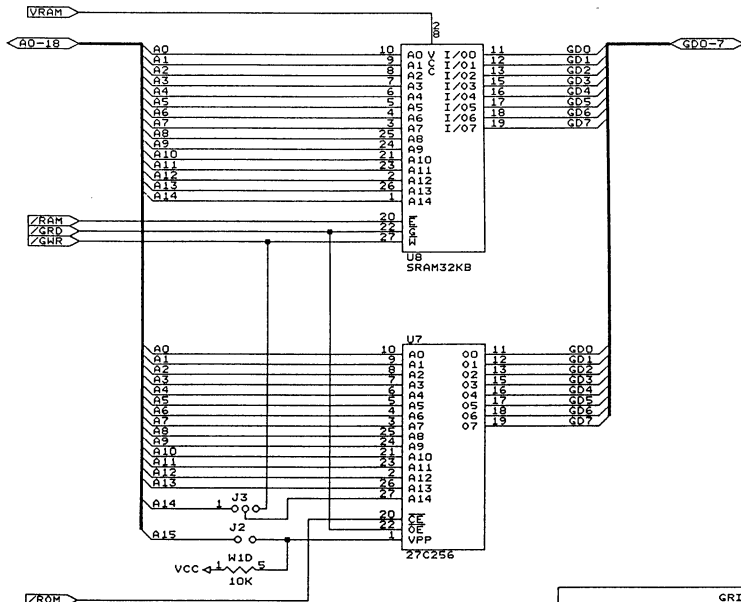
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Date: August 9, 1990 Sheet

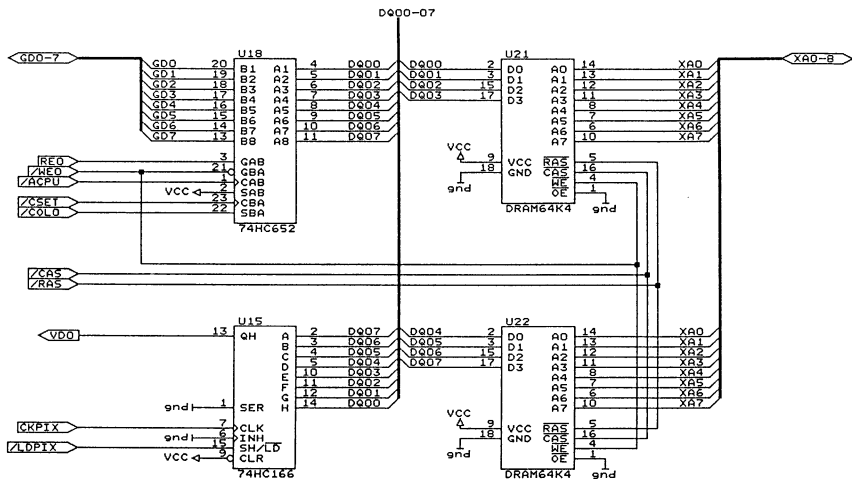
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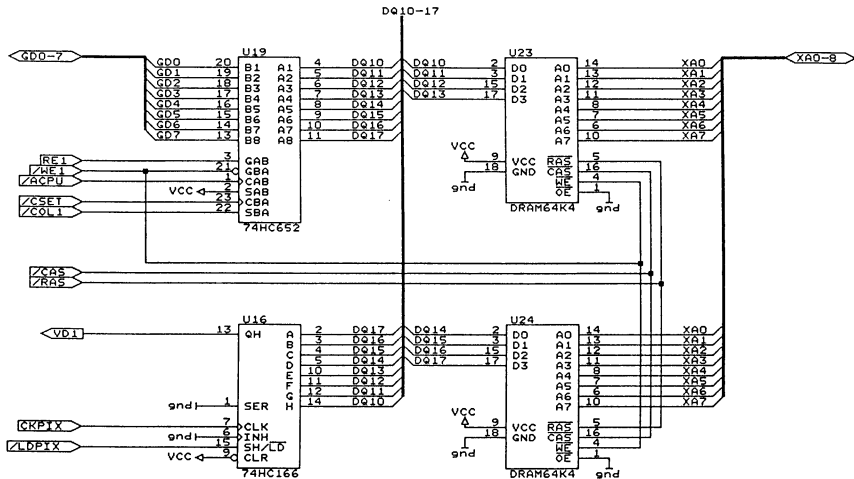
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Date:	August 9, 1990	Sheet	4 of 12



GRIP 6			
Title		Memory	
Size	Document Number	REV	
A	Conitec JCL	0	
Date:	August 9, 1990	Sheet	5 of 12



GRIP 6			
Title			
RAM			
Size	Document Number	REV	
A	Conitec JCL	0	
Date:	August 9, 1990	Sheet	6 of 12



GRIP 6

Title

RAM 1

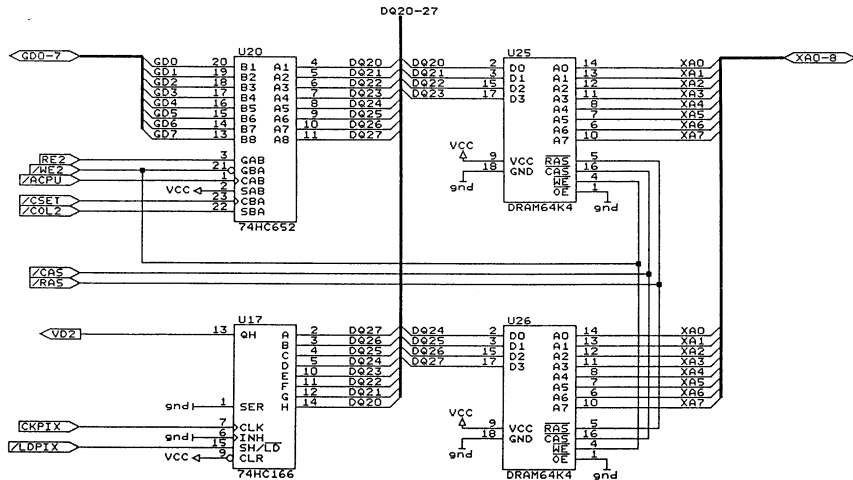
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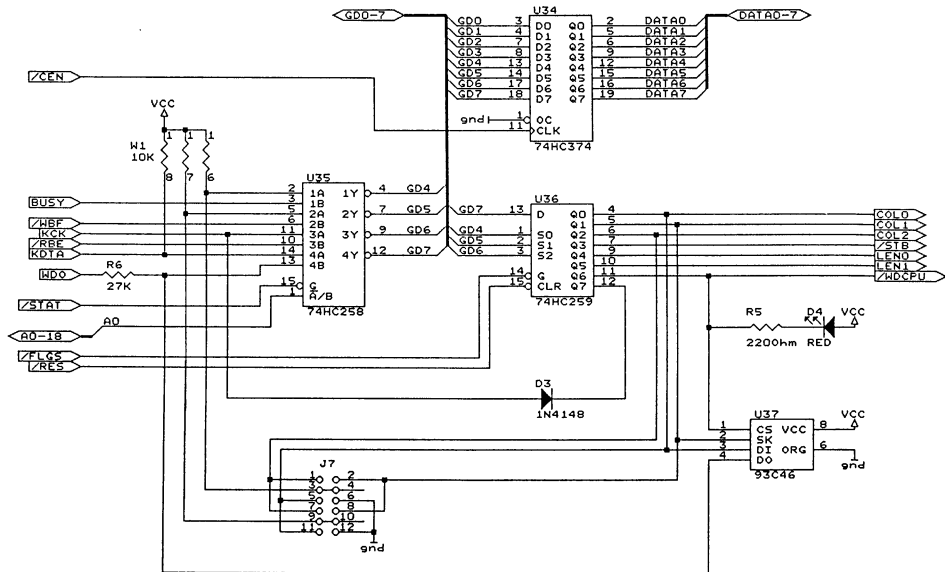
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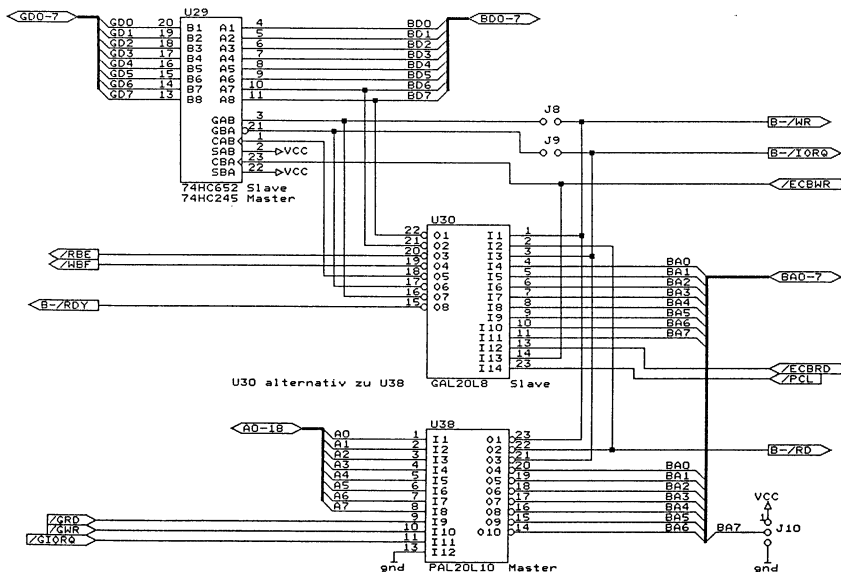
Date: August 9, 1990 Sheet 7 of 12



GRIP 6			
Title			
RAM 2			
Size	Document Number	REV	
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Date:	August 9, 1990	Sheet	8 of 12



GRIP 6			
Title		Ports	
Size	Document Number	REV	
A	Conitec JCL	0	
Date:	August 10, 1990	Sheet	9 of 12



U30 alternativ zu U38

Slave

Master

GRIP 6

Title

ECB

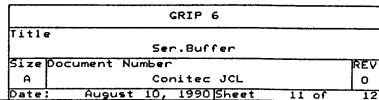
Size Document Number

REV

A Conitec JCL

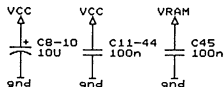
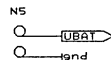
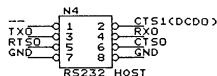
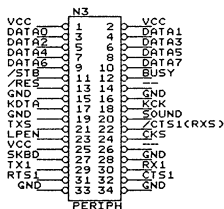
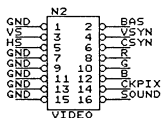
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Date: August 10, 1990 Sheet 10 of 12

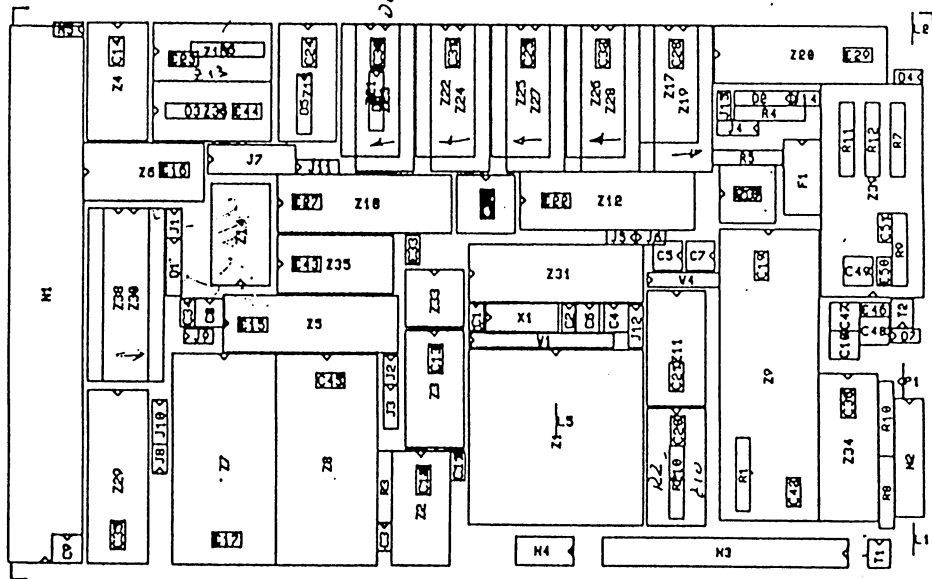


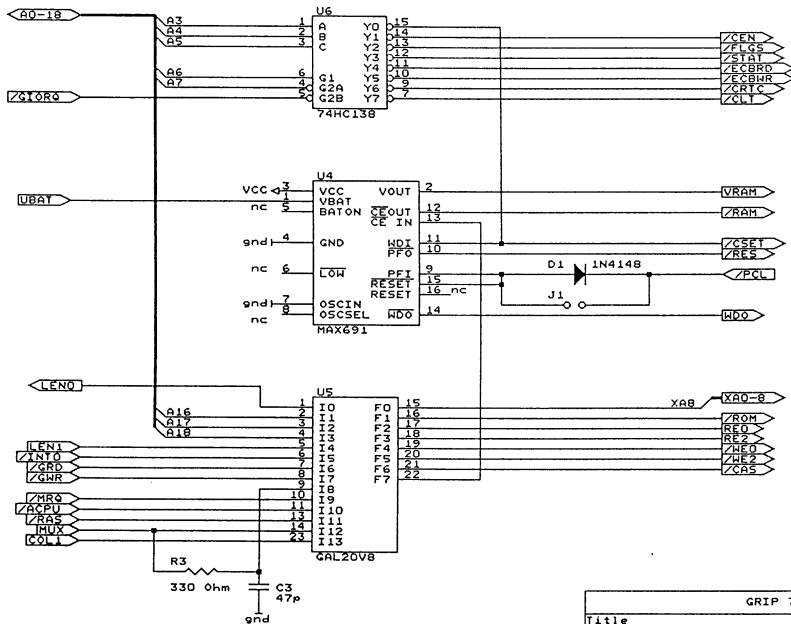
N1 ECB-Bus-Stecker

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01	VCC	VCC	VCC
02	B-D5	---	B-D0
03	B-D6	/GRD	B-D7
04	B-D3	/GWR	B-D2
05	B-D4	GD0	B-A0
06	B-A2	GD1	B-A3
07	B-A4	XA6	B-A1
08	B-A5	XA4	---
09	B-A6	GD2	B-A7
10	---	GD3	---
11	---	XA0	IEI.....
12	---	XA1	---
13	---	A18	---
14	---	GD4	B-D1
15	TX0	GD5	DATA7
16	RTS0	XA5	IEQ.....
17	---	XA7	---
18	---	GD6	---
19	RX0	GD7	---
20	---	/CA5	---
21	B-SKBD	XA3	---
22	CTS0	XA2	B-/WR
23	---	/INT0	/WDPCPU
24	UBAT	/GIORQ	B-/RD
25	---	/RAS	---
26	B-/RDY	VID	/PCL
27	B-/IORQ	VSYN	---
28	---	HSYN	---
29	---	/ACPU	---
30	---	/LDPIX	---
31	---	CKPIX	---
32	GND	GND	GND

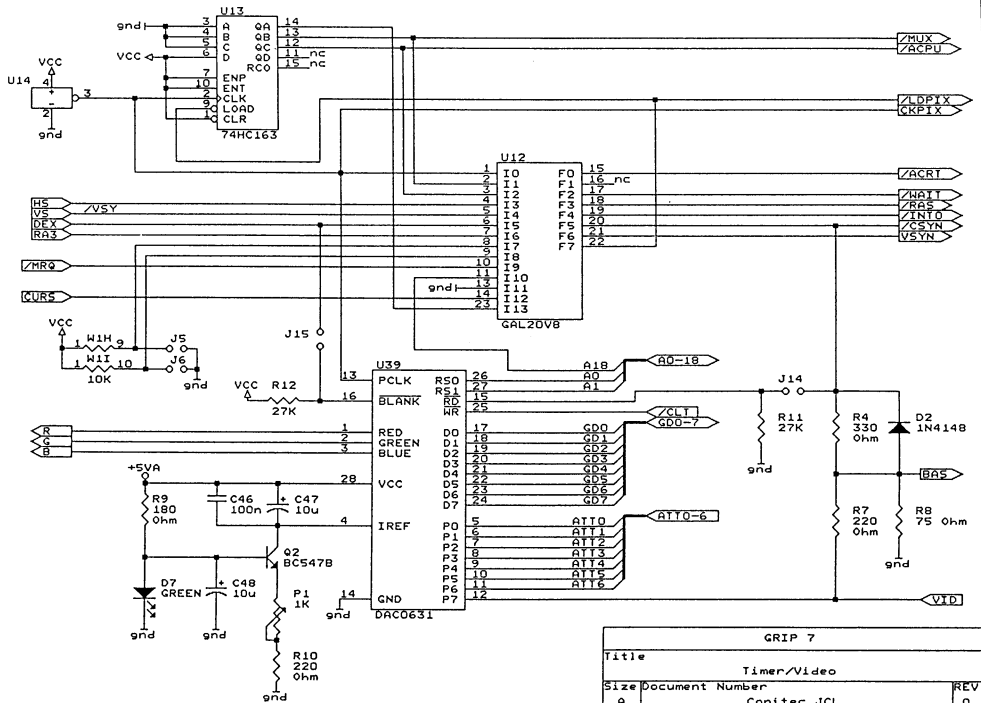


GRIP 6			
Title			
Plug			
Size	Document Number		REV
A	Conitec JCL		0
Date:	August 10, 1990 Sheet 12 of 12		

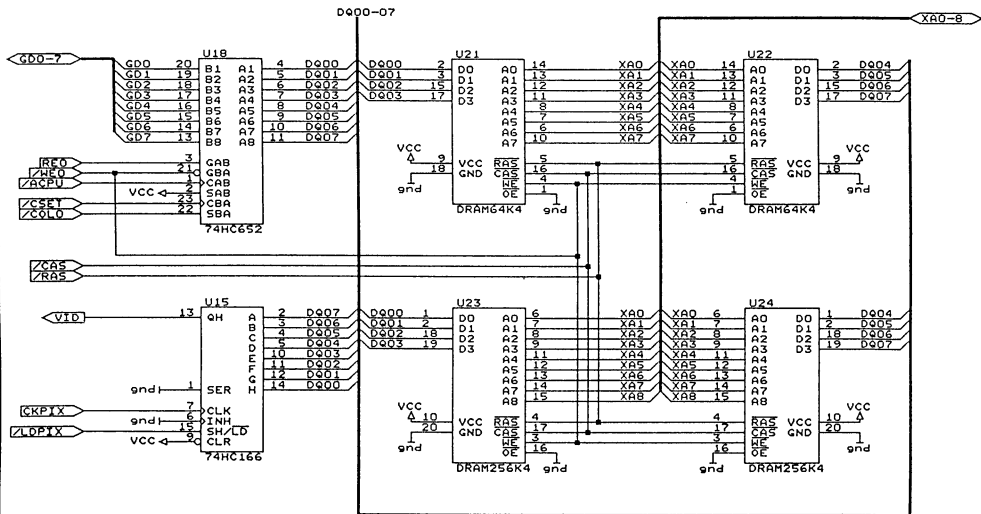




GRIP 7			
Title			
Decoder			
Size	Document Number	REV	
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Date:	July 30, 1990	Sheet	2 of 11

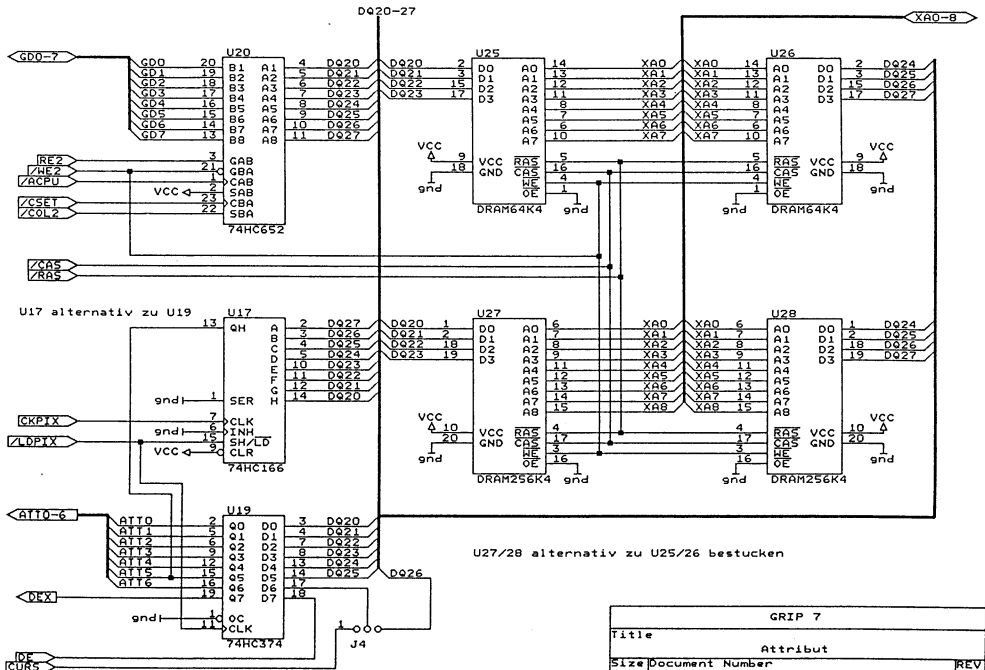


GRIP 7			
Title			
Timer/Video			
Size	Document Number		REV
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Date:	July 30, 1980	Sheet	3 of 11



U23/24 alternativ zu U21/22 bestücken

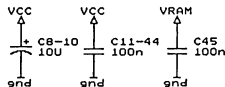
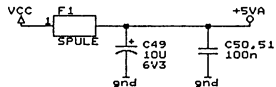
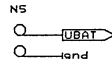
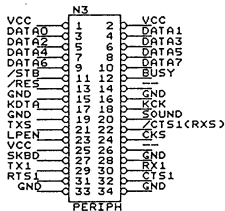
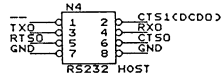
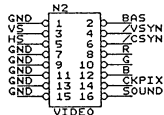
GRIP 7			
Title			
RAM			
Size	Document Number		REV
A	Conitec JCL		0
Date:	August 9, 1990	Sheet	6 of 11



GRIP 7			
Title		Attribut	
Size Document Number		REV	
A	Conitec JCL	O	
Date:	August 9, 1990	Sheet	7 of 11

N1 ECB-Bus-Stecker

	a	b	c
01	VCC	VCC	VCC
02	B-D5	---	B-D0
03	B-D6	/GRD	B-D7
04	B-D3	/GWR	B-D2
05	B-D4	GDO	B-A0
06	B-A2	GD1	B-A3
07	B-A4	XA6	B-A1
08	B-A5	XA4	---
09	B-A6	GD2	B-A7
10	---	GD3	---
11	---	XA0	IEI
12	---	XA1	---
13	---	---	A18
14	---	GD4	B-D1
15	---	GD5	---
16	---	XA5	IEQ...
17	---	XA7	---
18	---	GD6	---
19	---	GD7	---
20	---	/CAS	---
21	B-SKBD	XA3	---
22	---	XA2	B-/WR
23	---	/INT0	---
24	UBAT	/GIORQ	B-/RD
25	---	/RAS	---
26	B-/RDY	VID	/PCL
27	B-/IORQ	/VSY	---
28	---	/CSYN	---
29	---	/ACPU	---
30	---	/LDPIX	---
31	---	CKPIX	---
32	GND	GND	GND



GRIP 7			
Title			
Plug / Circuit			
Size	Document Number	REV	
A	Conitec JCL	0	
Date:	July 30, 1990	Sheet	11 of 11